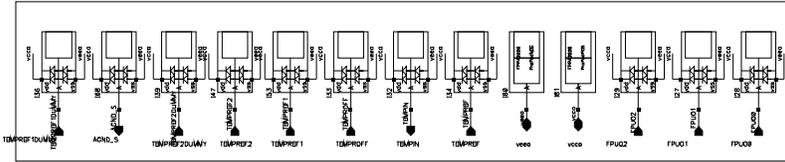
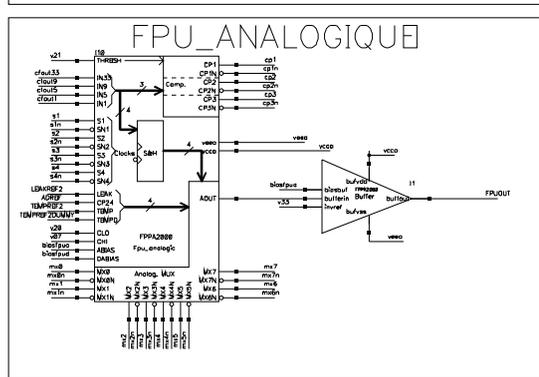
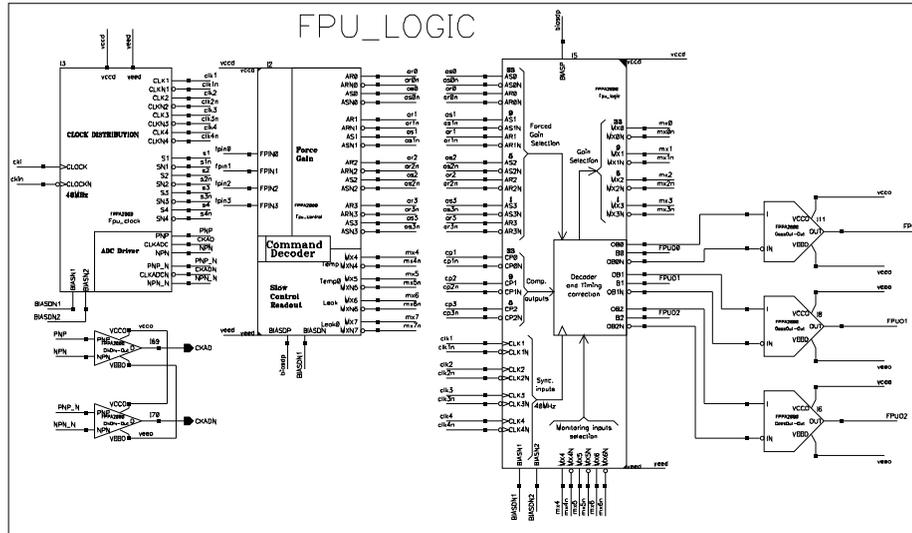
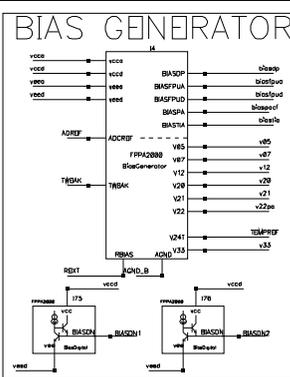
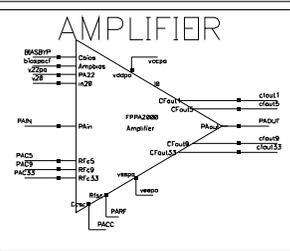
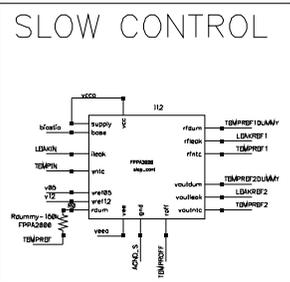
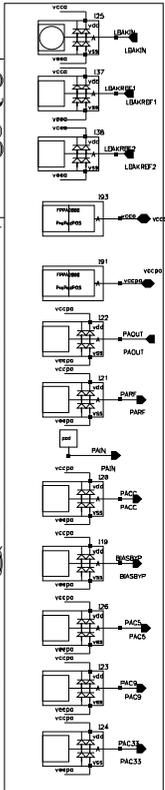


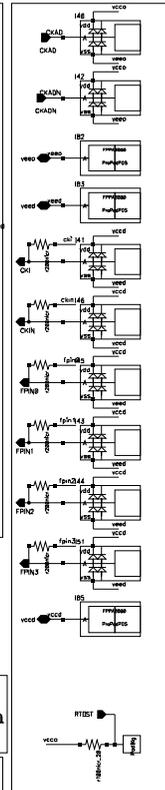
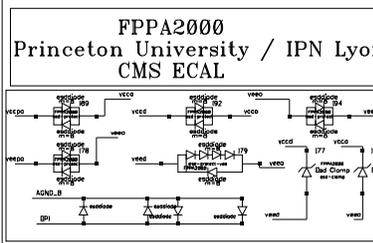
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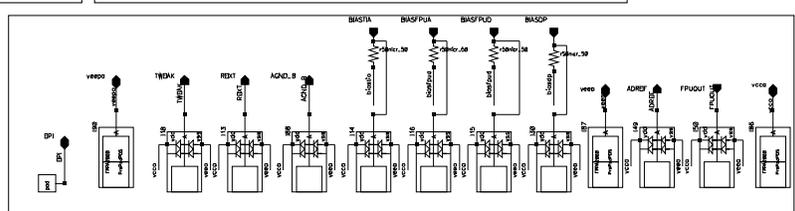
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CMS-BCAL	PrincetonUniversity/IPNL
Project	Floating Point PreAmplifier
Library Name	FPPA2000
Block Name	FPPA2000
Last Changed	
Preliminary Design Review	TRUB
Critical Design Review	TRUB

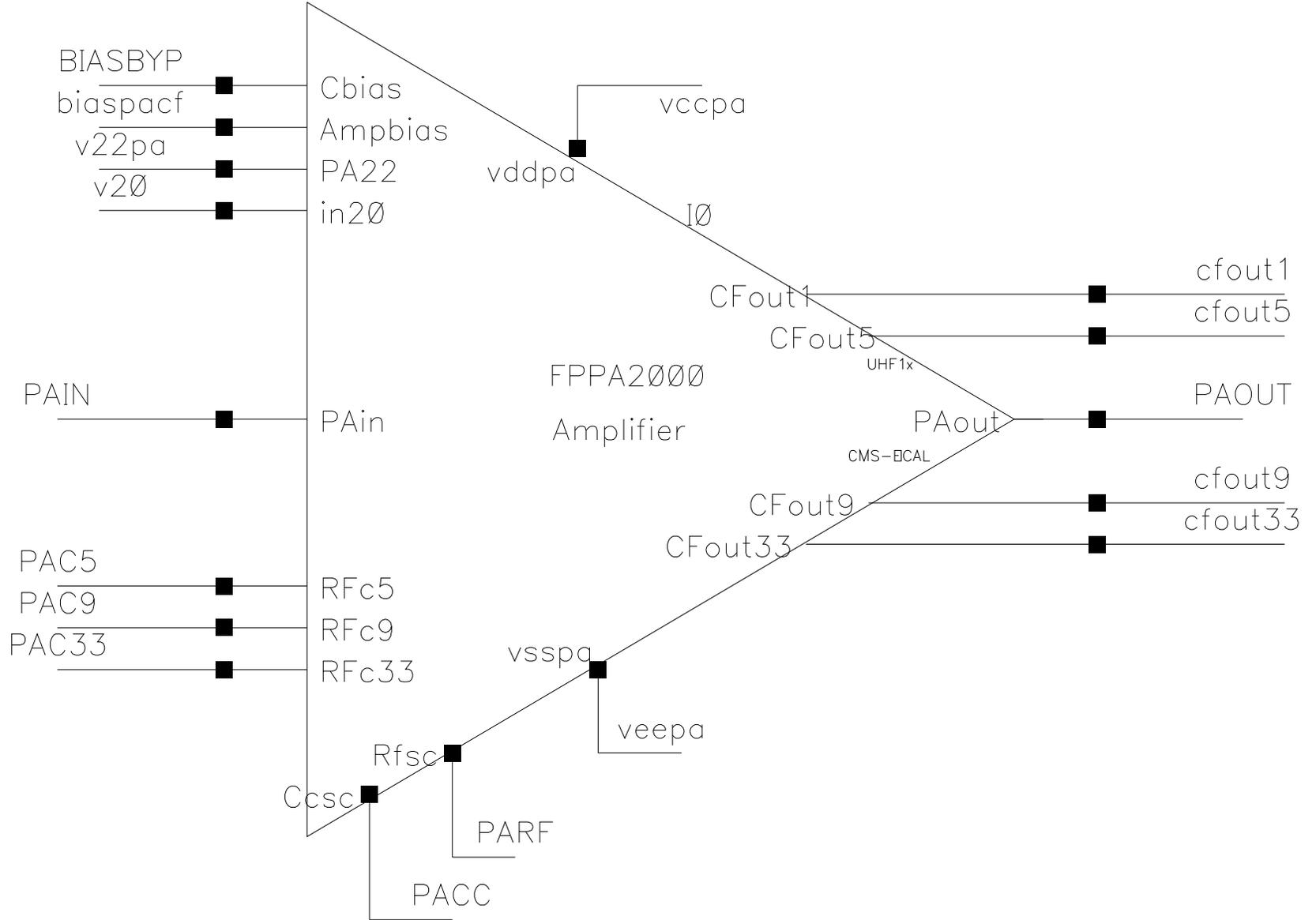


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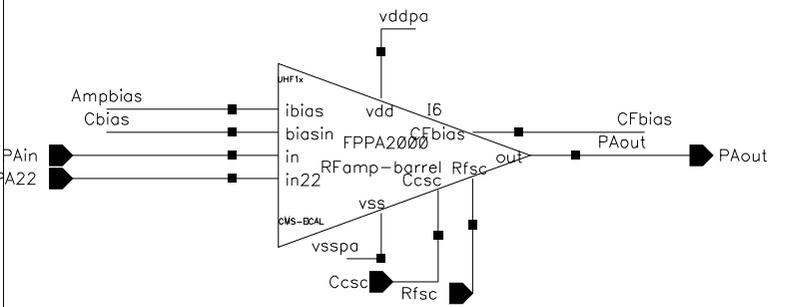
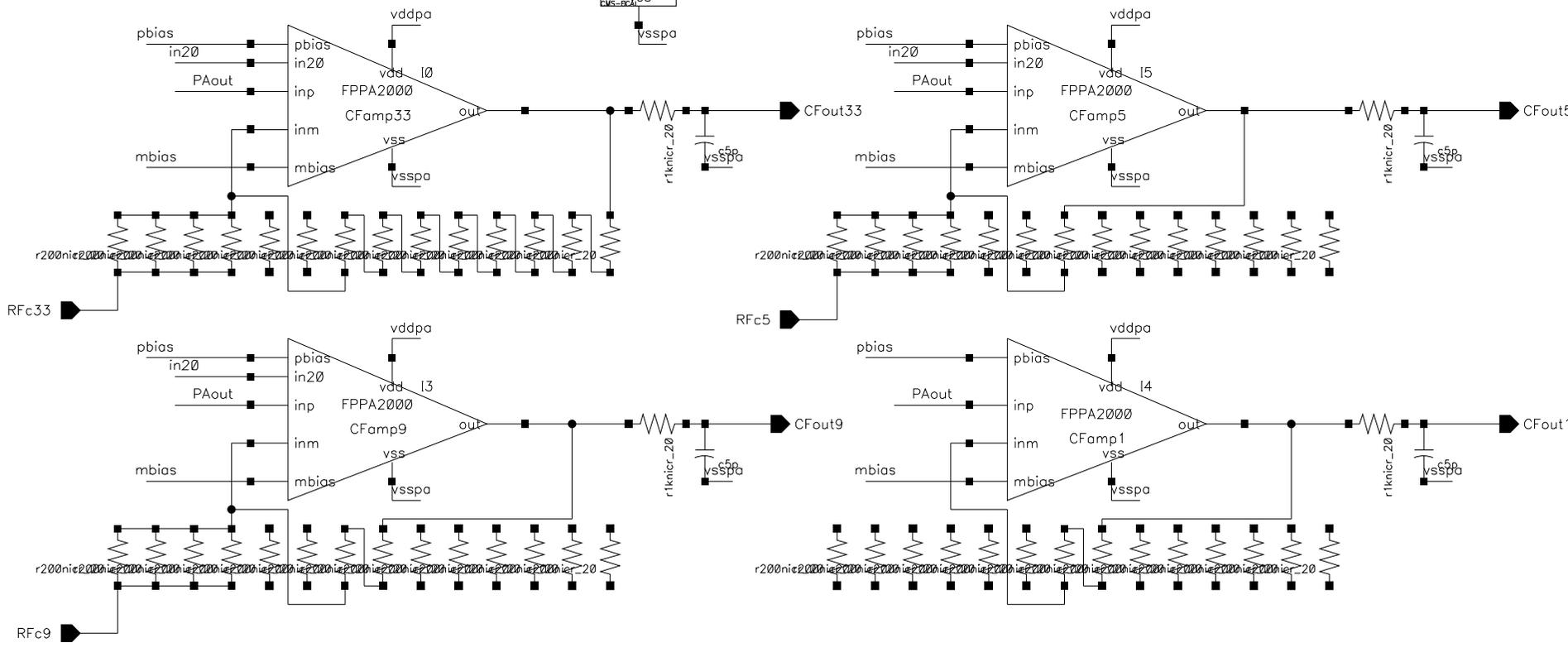
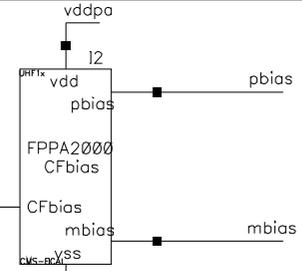
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AMPLIFIER BARREL

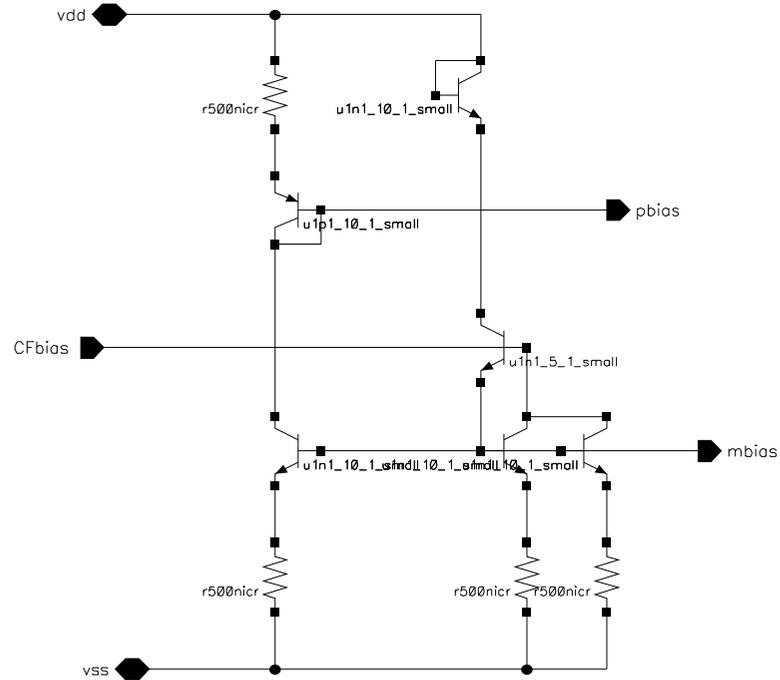


- vddpa
- vsspa

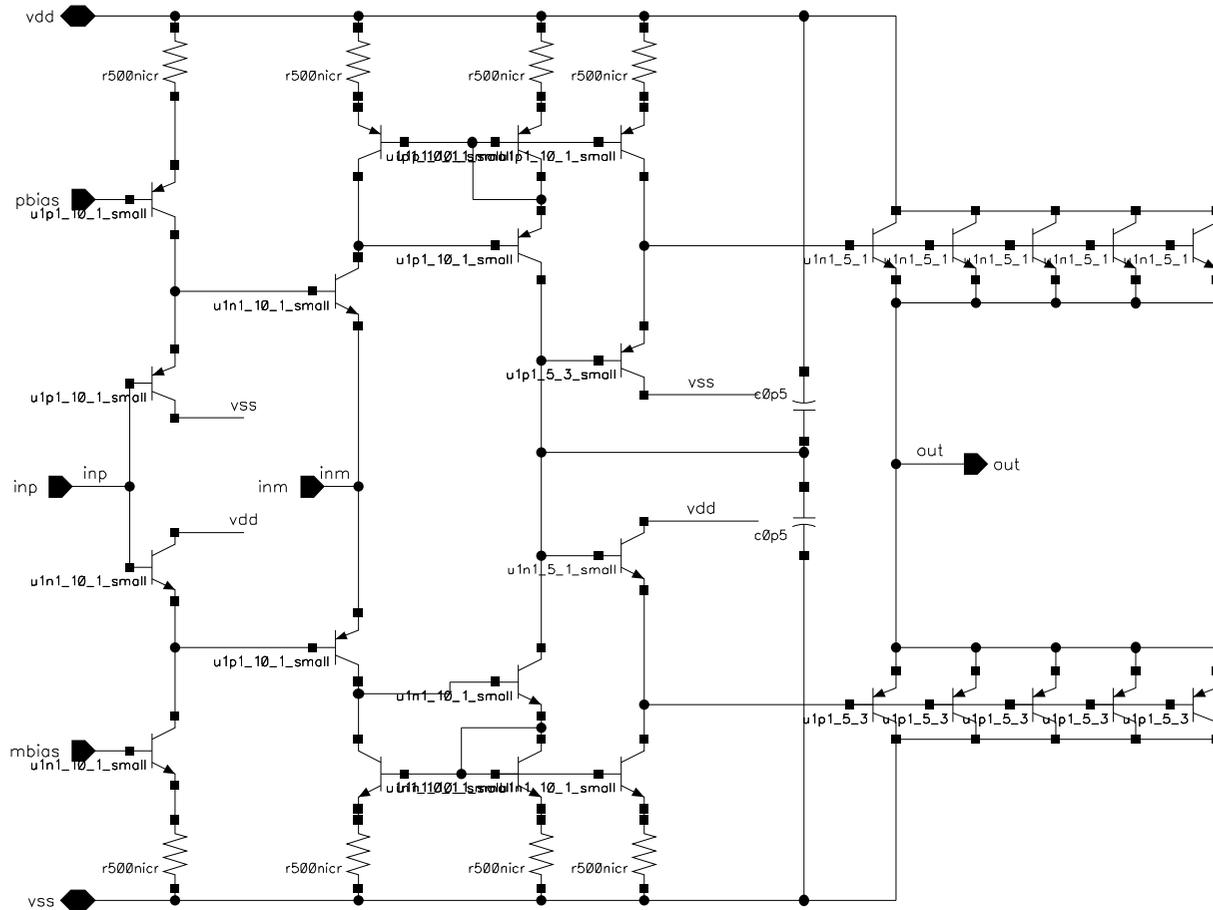
- Cbias
- Ambias
- in20



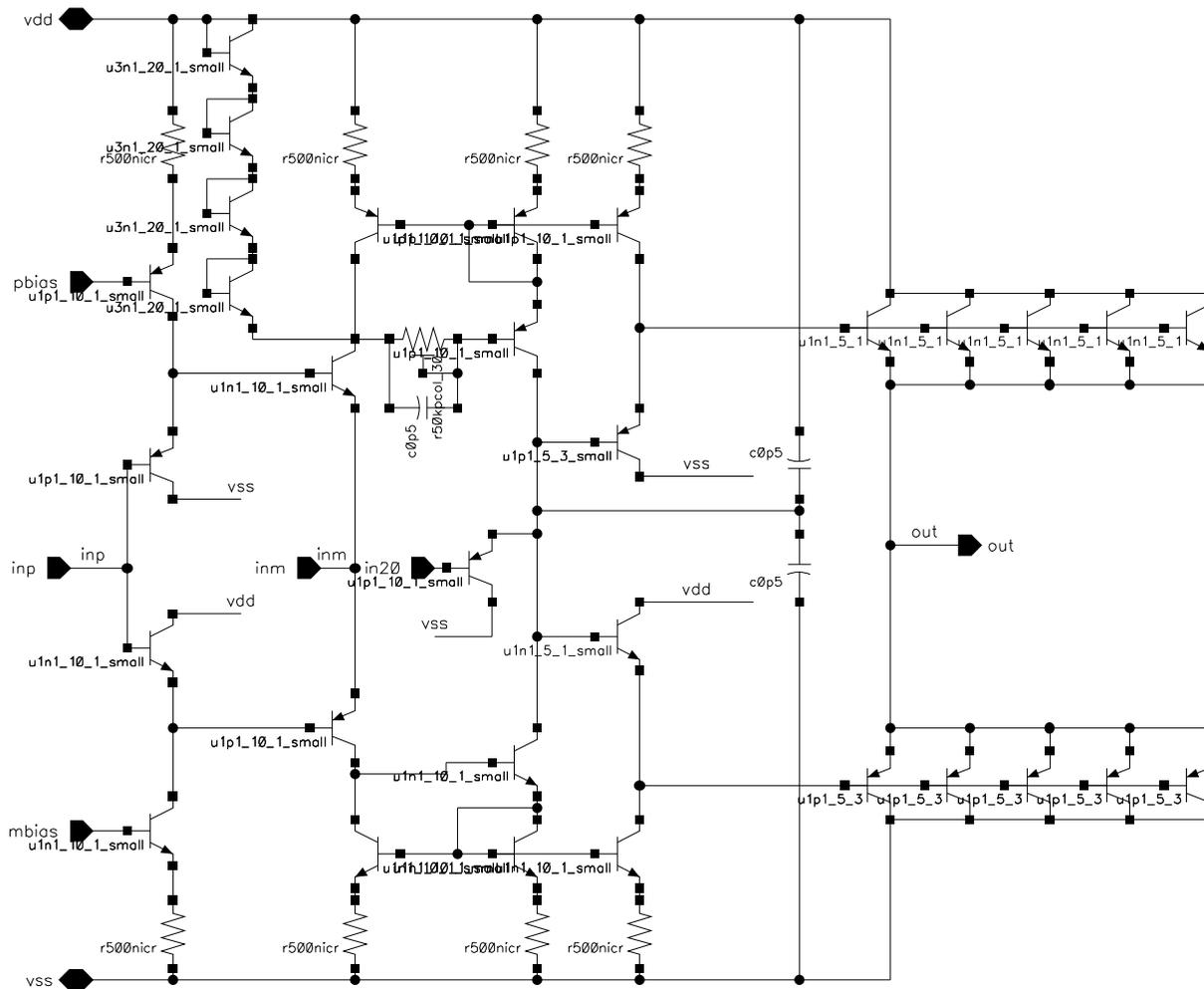
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	Amplifier → FPPA2000		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



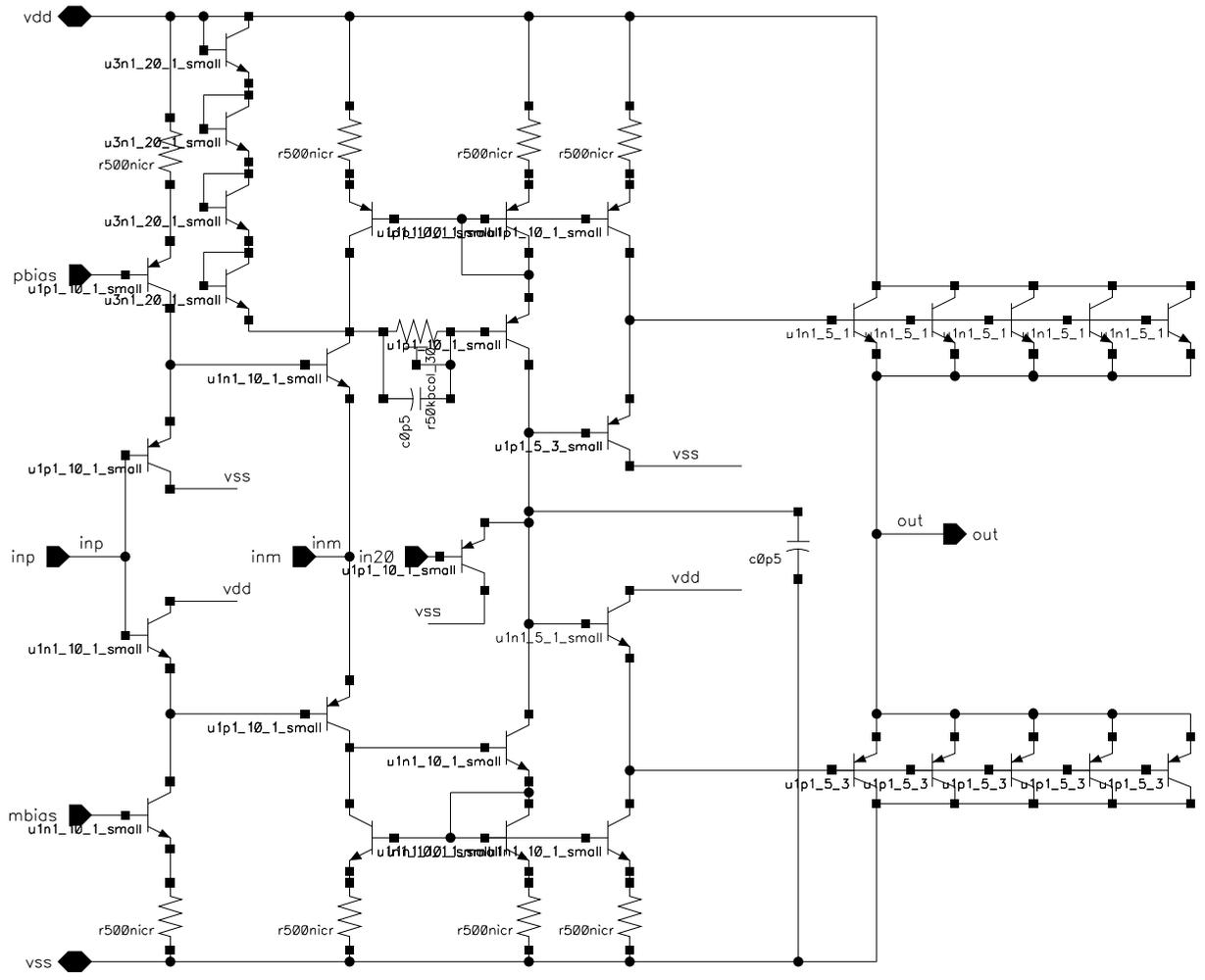
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	CFbias -> FPPA2000/Amplifier		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



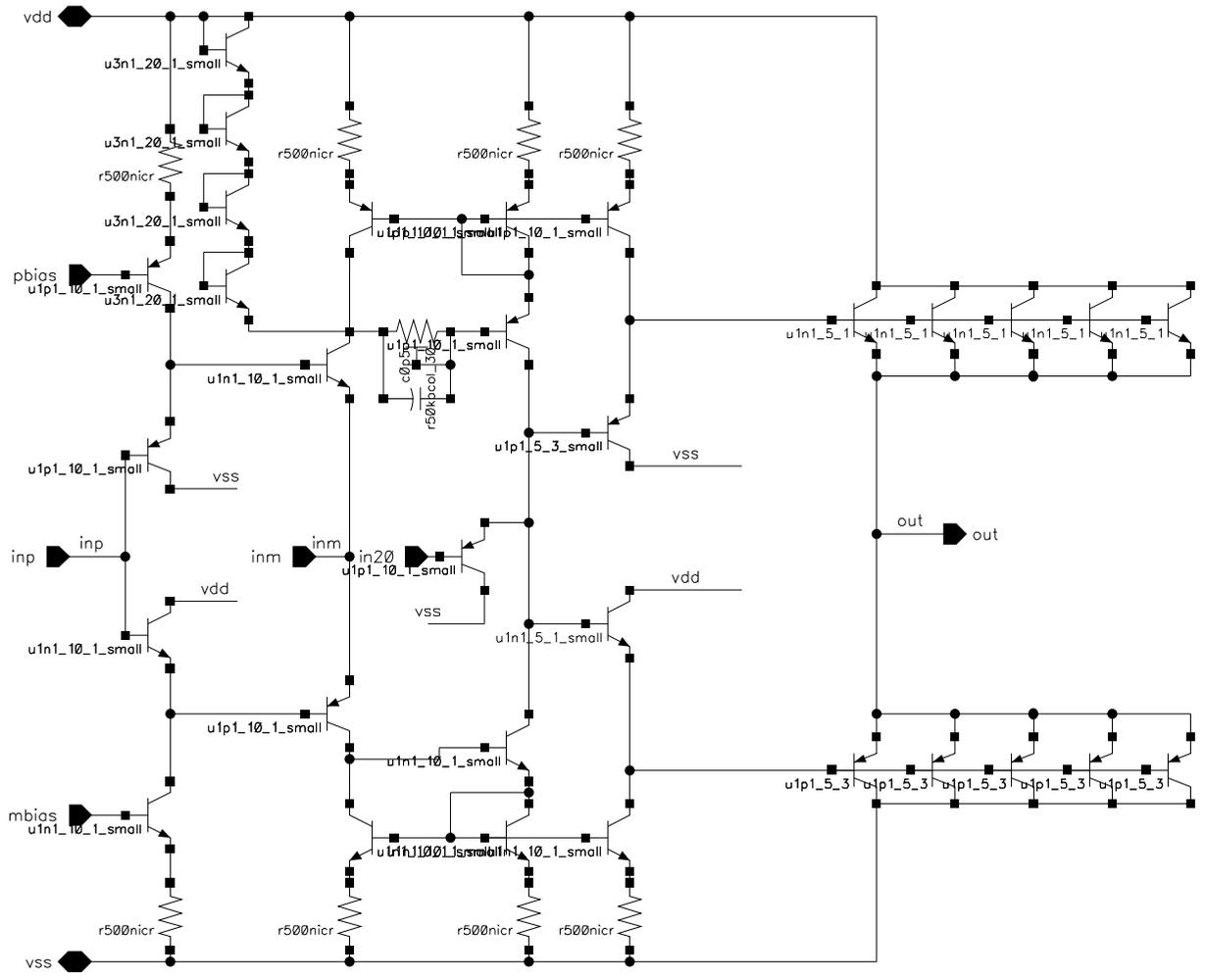
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	CFamp1 --> FPPA2000/Amplifier		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	CFamp5 -> FPPA2000/Amplifier		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE

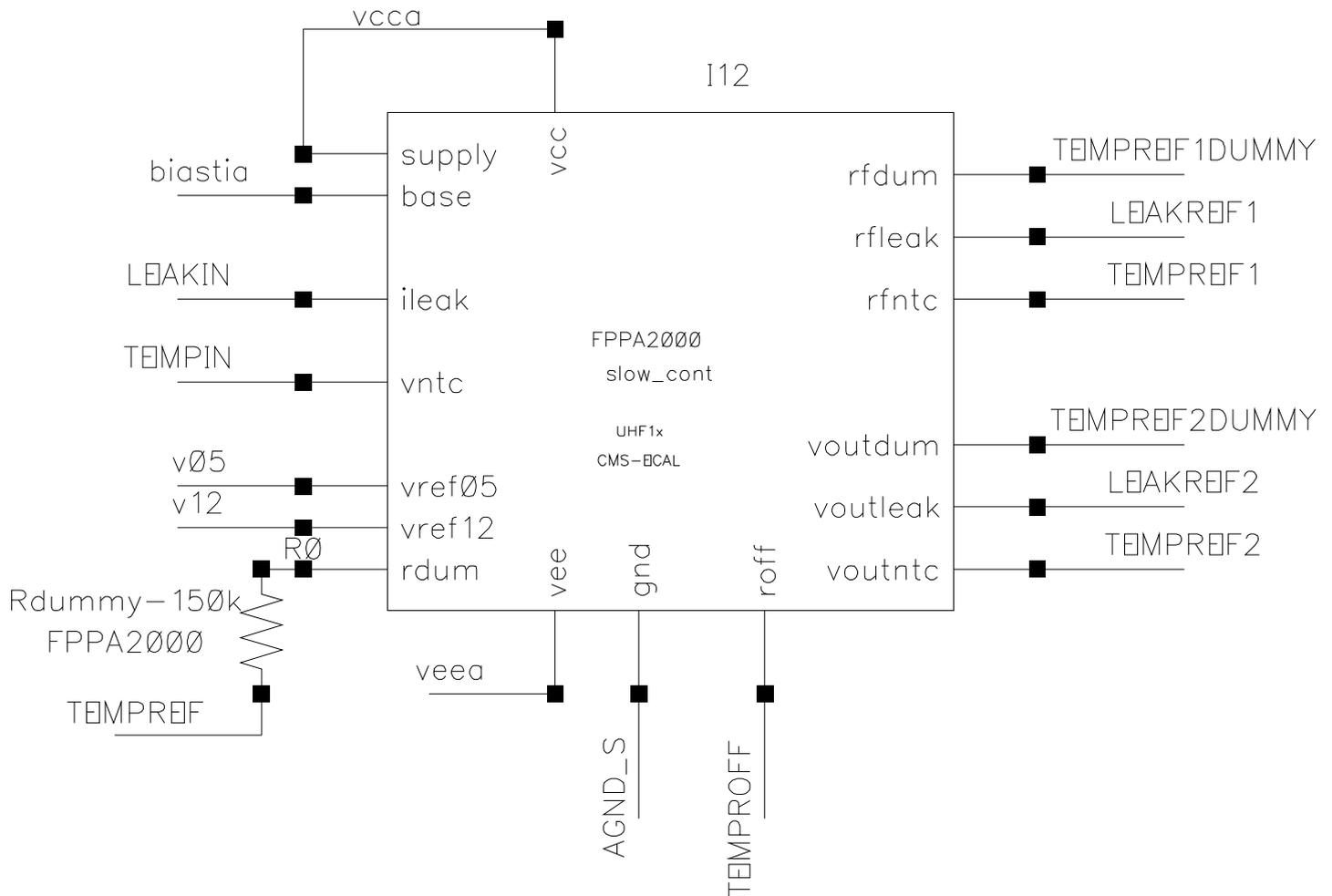


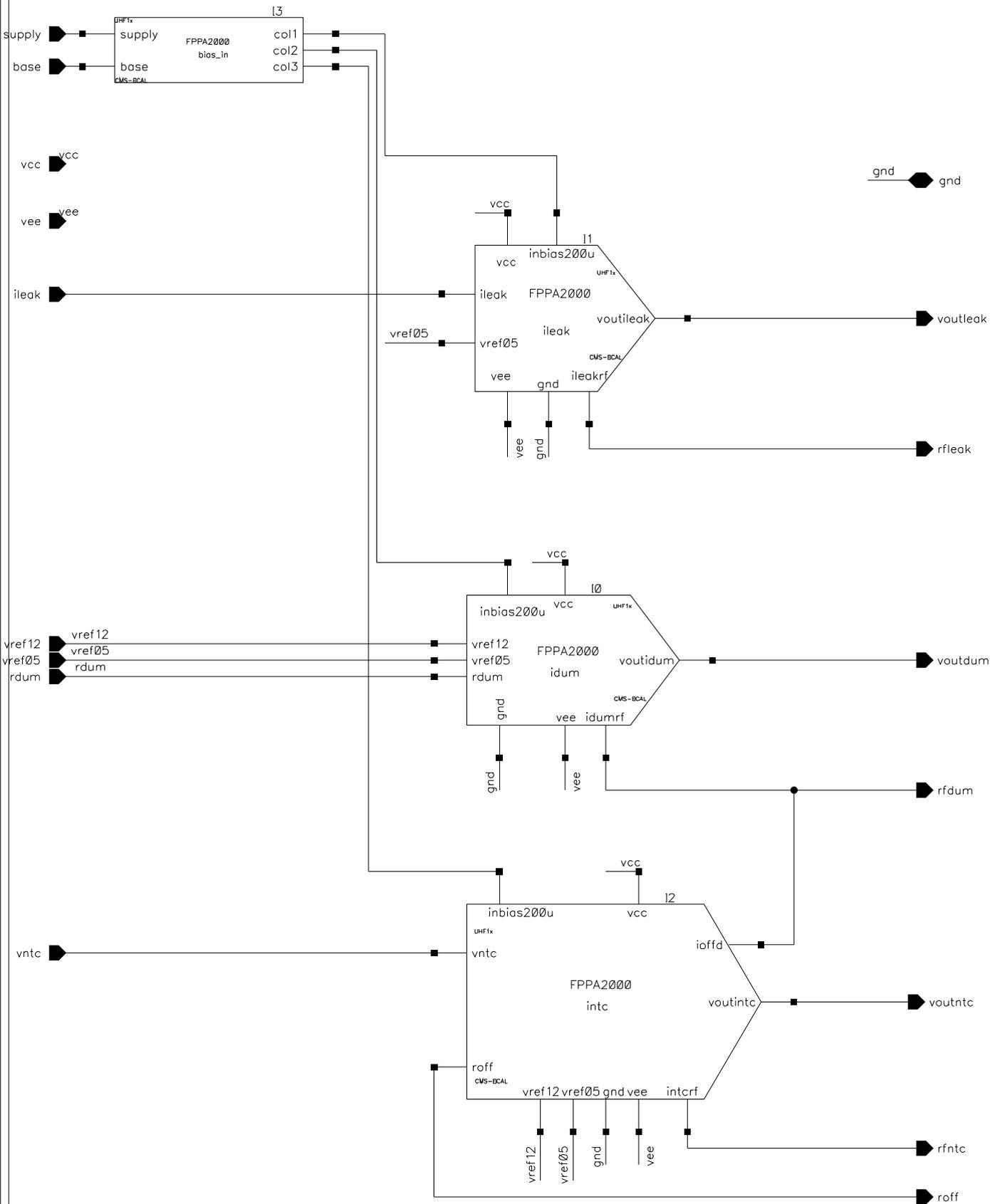
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	CFamp9 -> FPPA2000/Amplifier		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



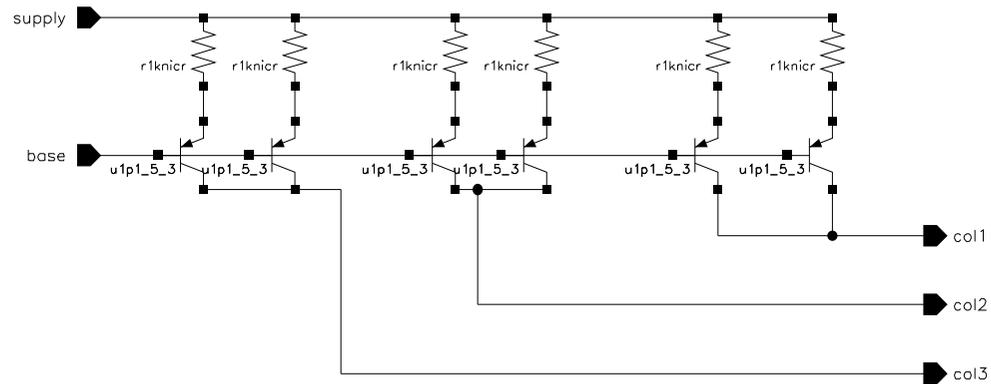
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	CFamp33 -> FPPA2000/Amplifier		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE

SLOW CONTROL

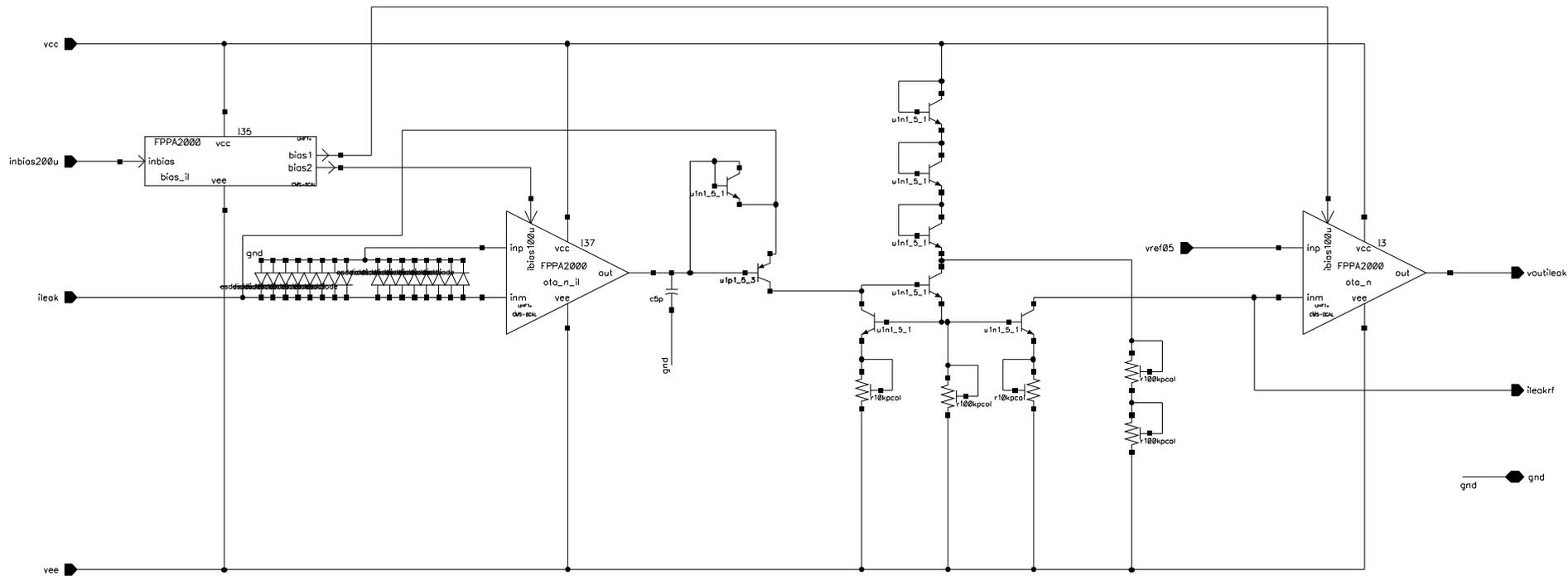




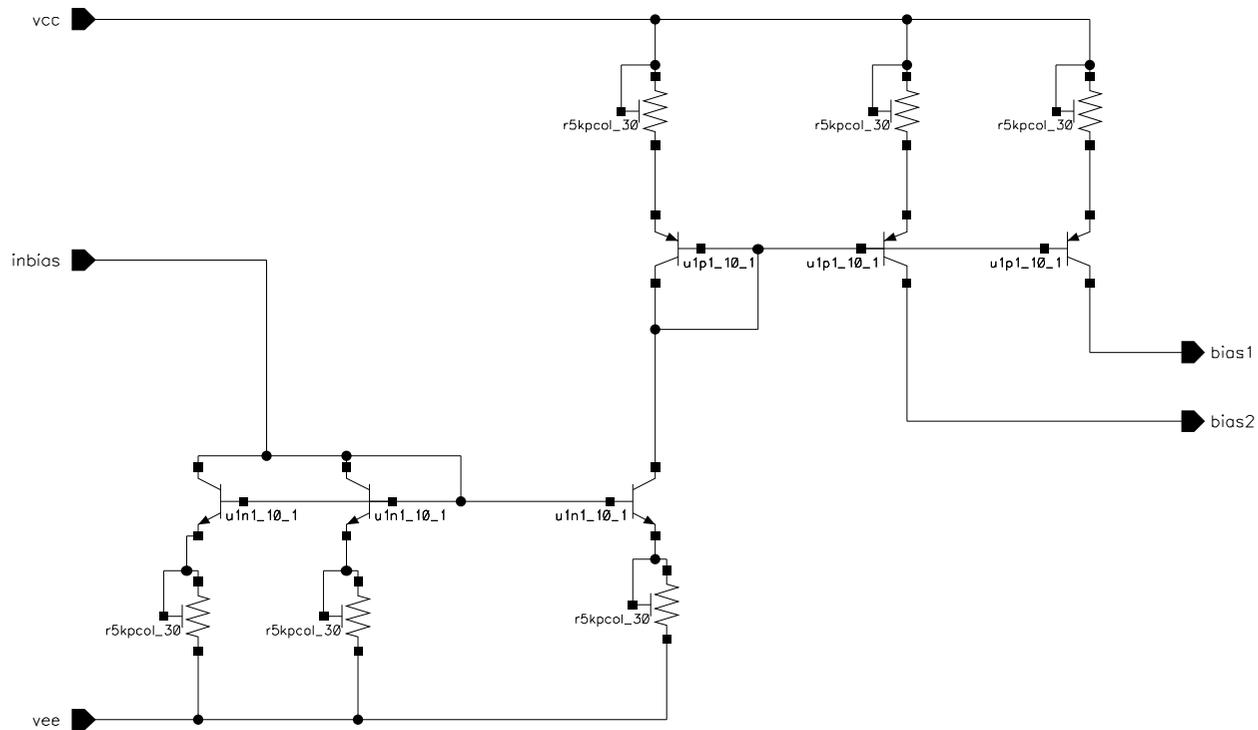
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	slow_cont	-> FPPA2000	
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



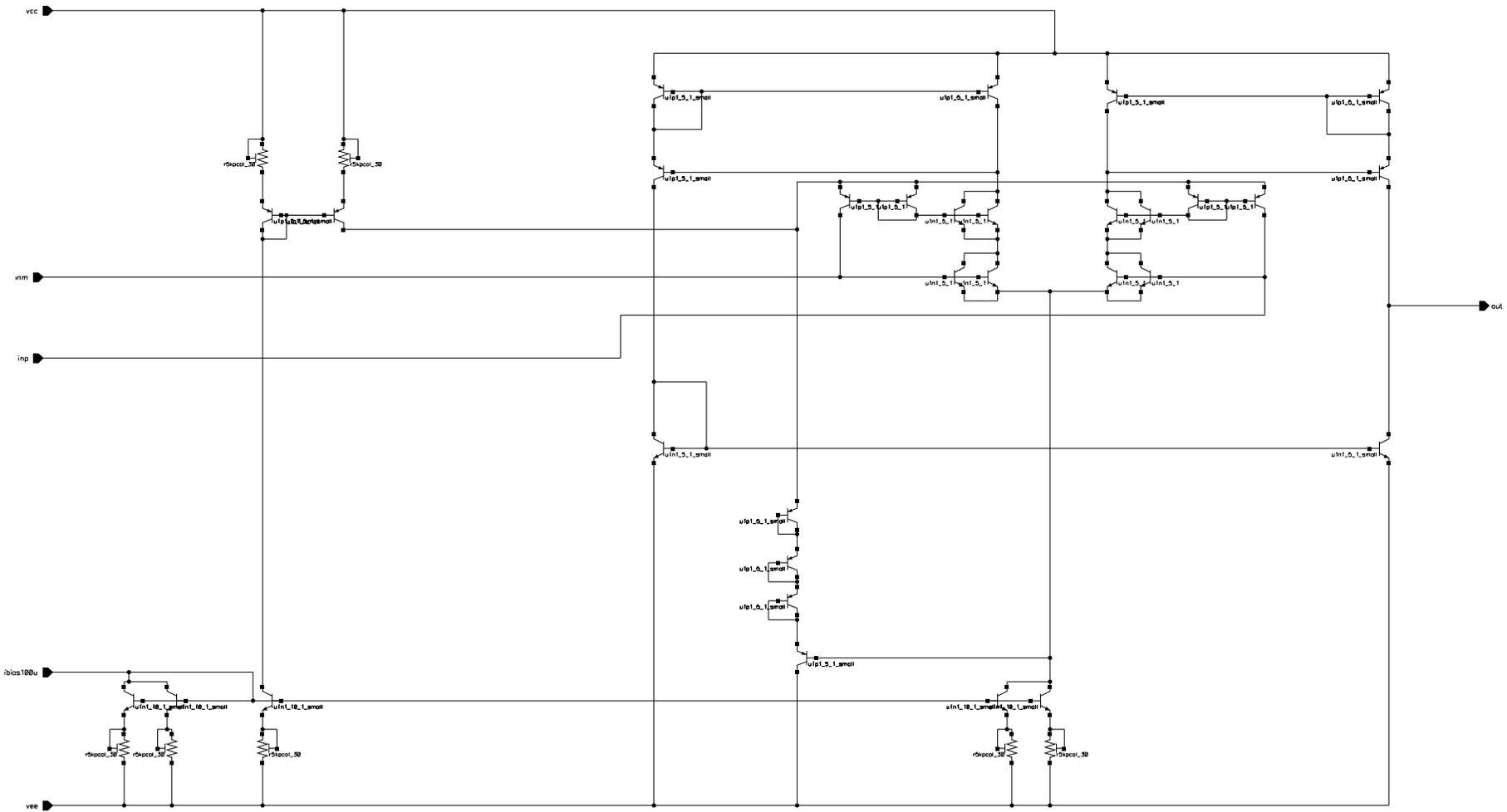
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Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	bias_in -> FPPA2000/slow_cont		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



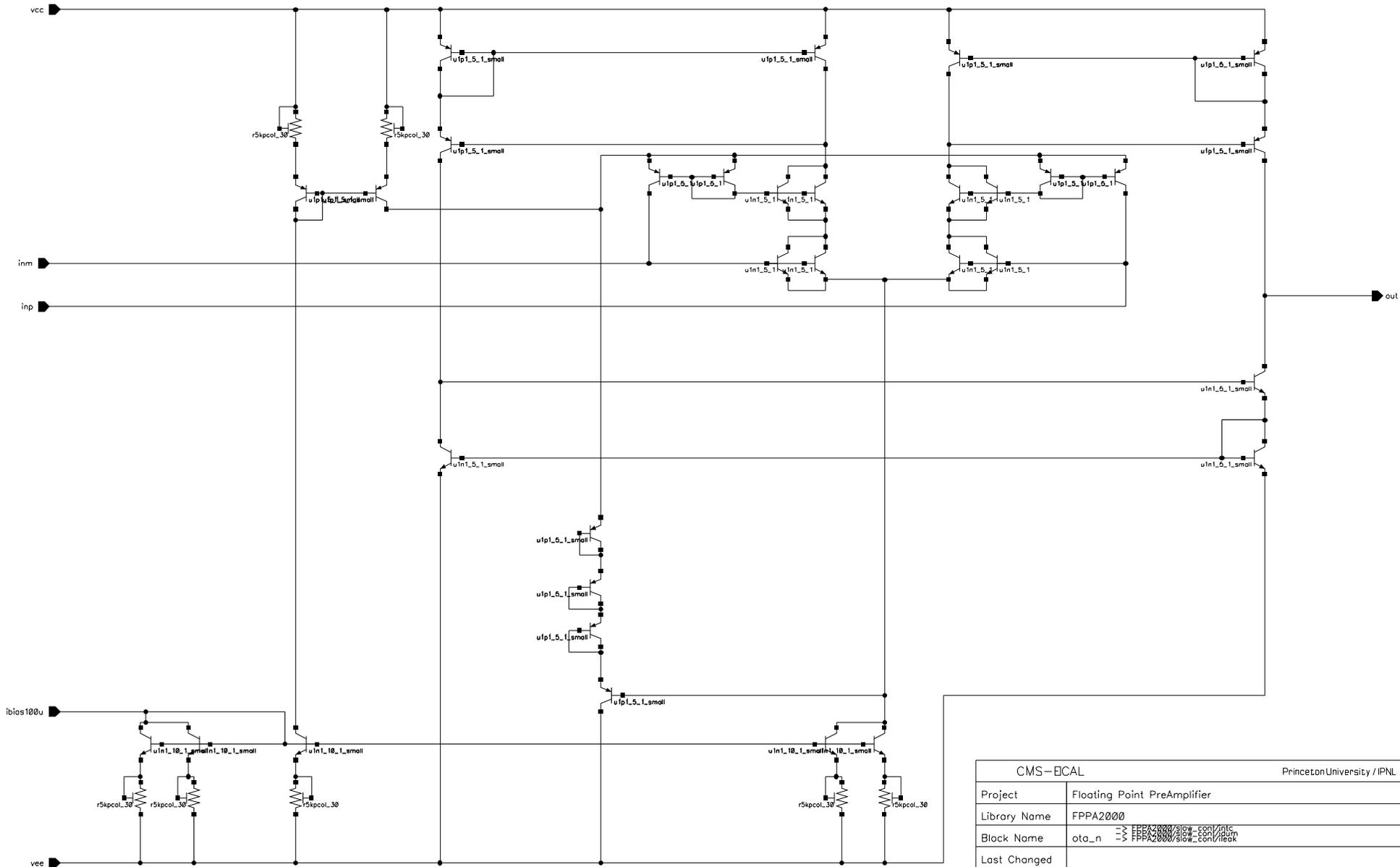
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	ileak -> FPPA2000/slow_cont		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



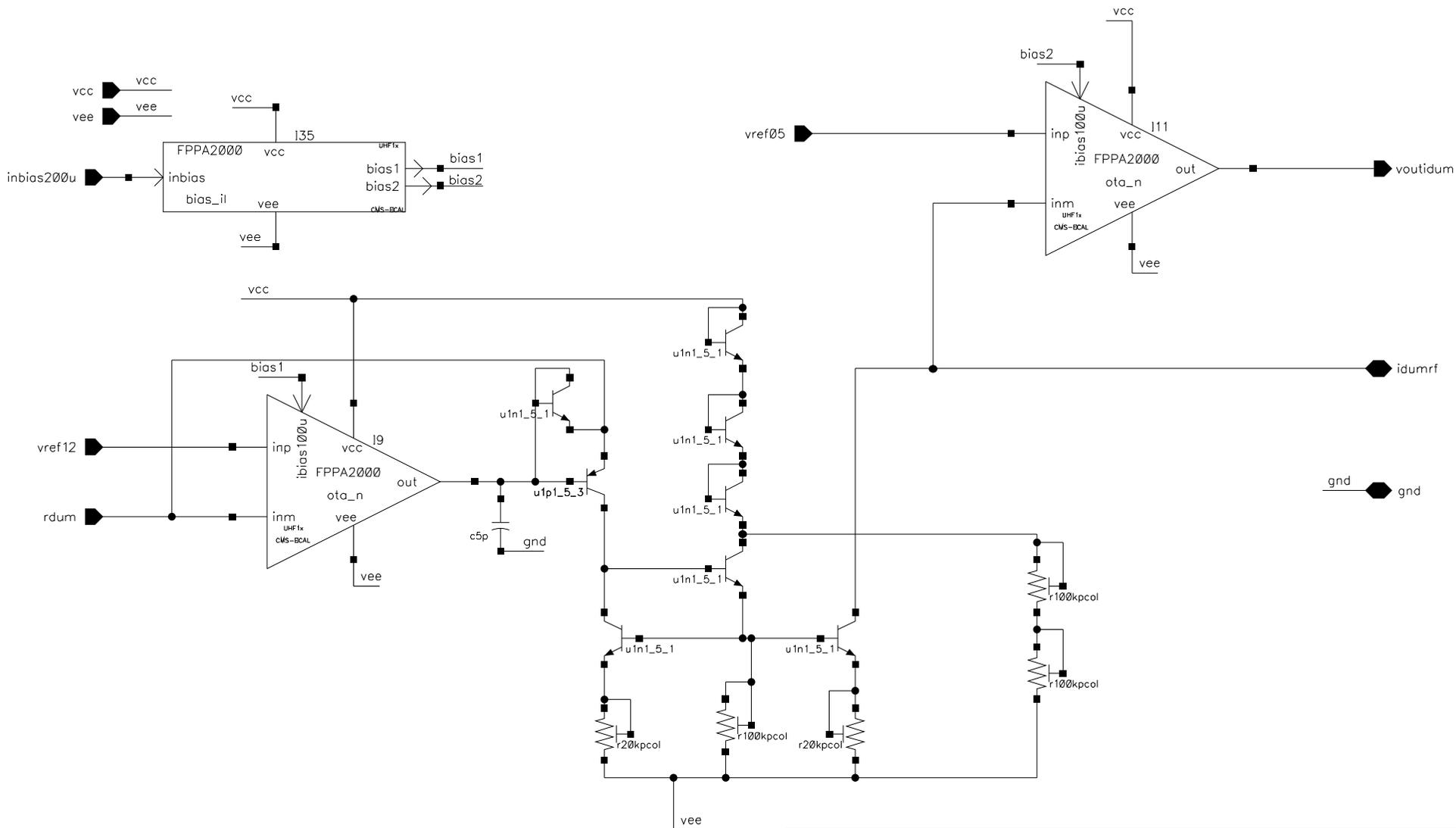
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	bias_il => FPPA2000/slow_cont/ldum FPPA2000/slow_cont/ileck		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



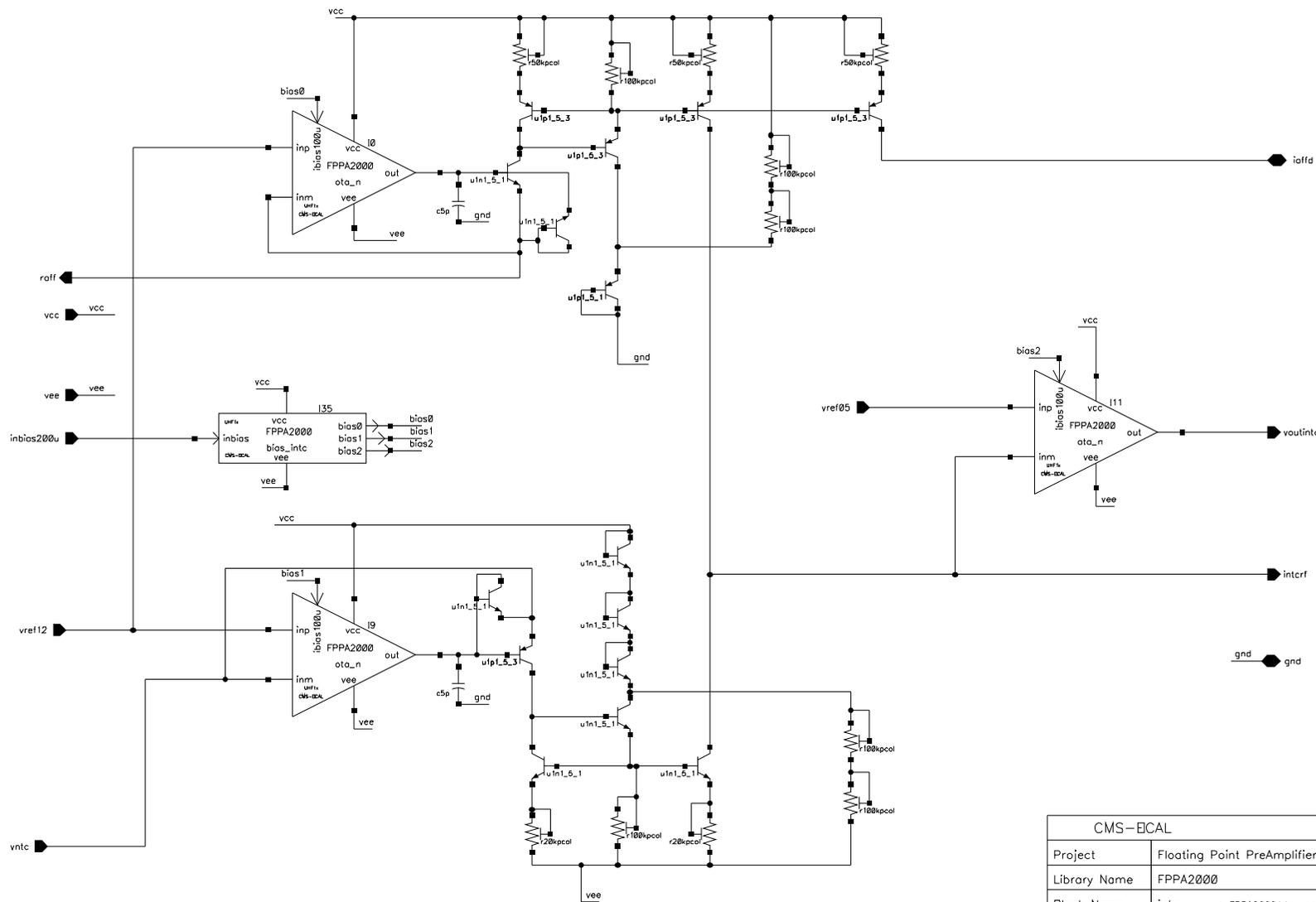
CMS-BCAL		Princeton University / IPNL	
Project	Floating Point Pre-Amplifier		
Library Name	FPPA2000		
Block Name	ata_n_1l -> FPPA2000/slow_cont/leak		
Last Changed			
Preliminary Design Review	TRUB	Critical Design Review	TRUB



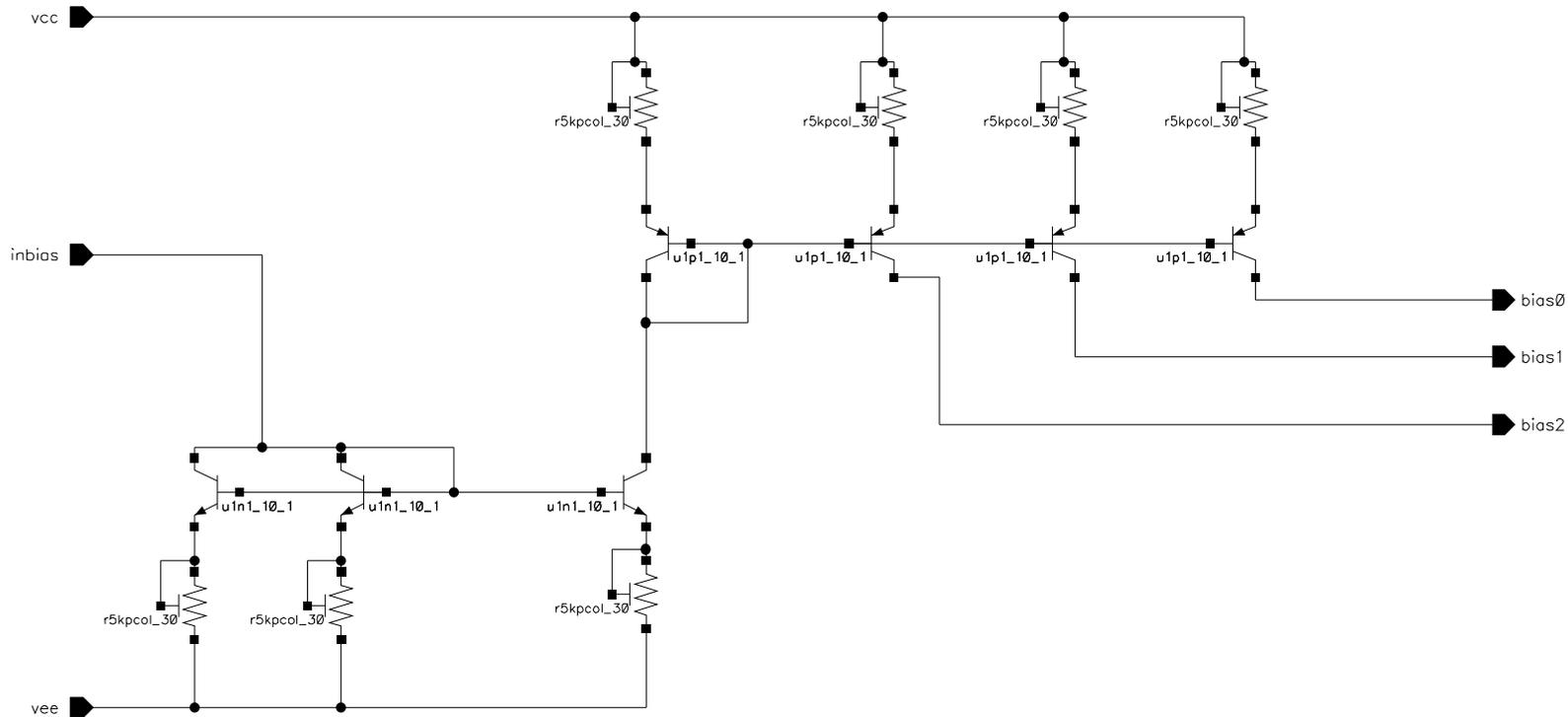
CMS-BCAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	ota_n -> FPPA2000/slew_con/otc -> FPPA2000/slew_con/otm -> FPPA2000/slew_con/leak		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	idum -> FPPA2000/slow_cont		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE

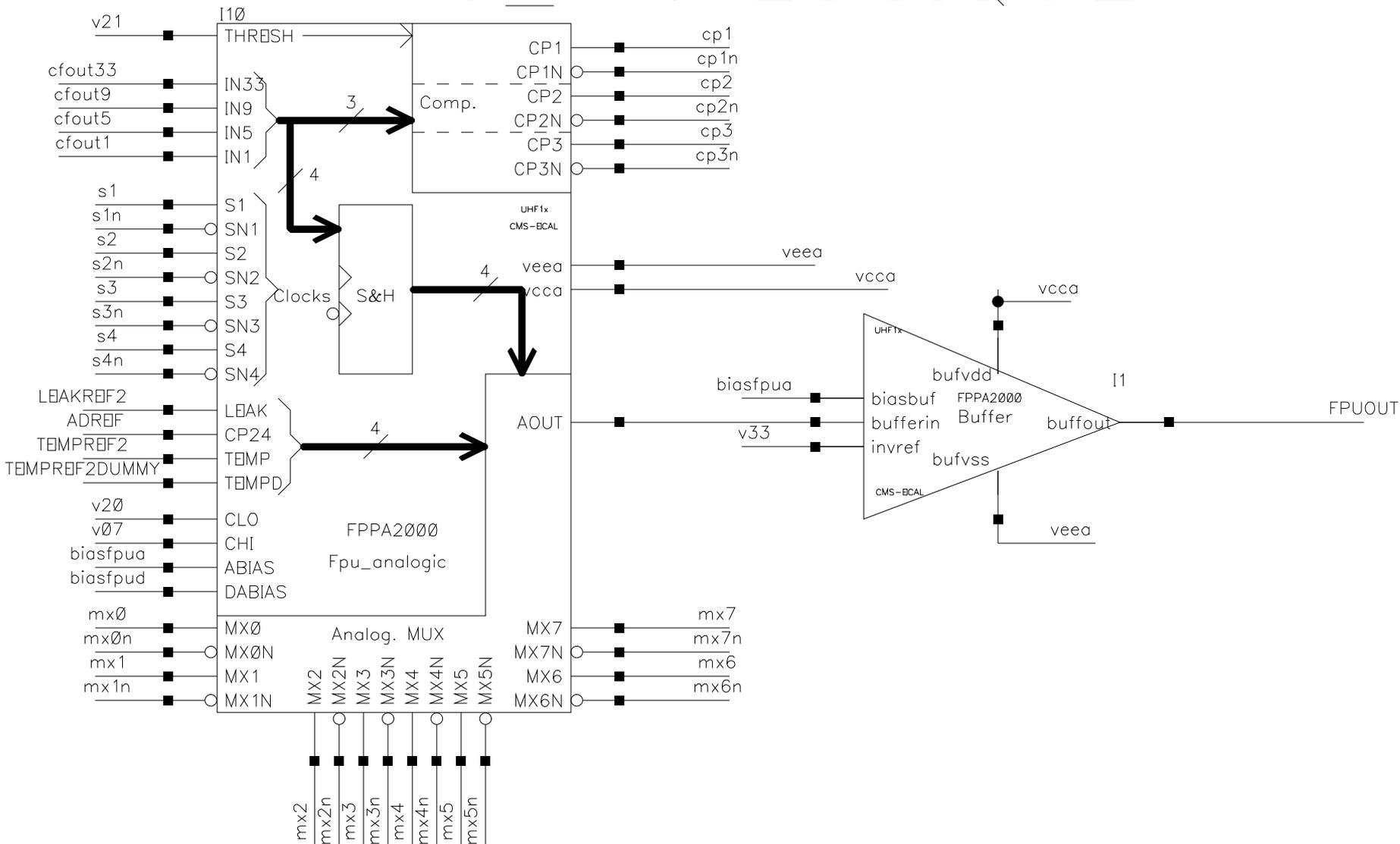


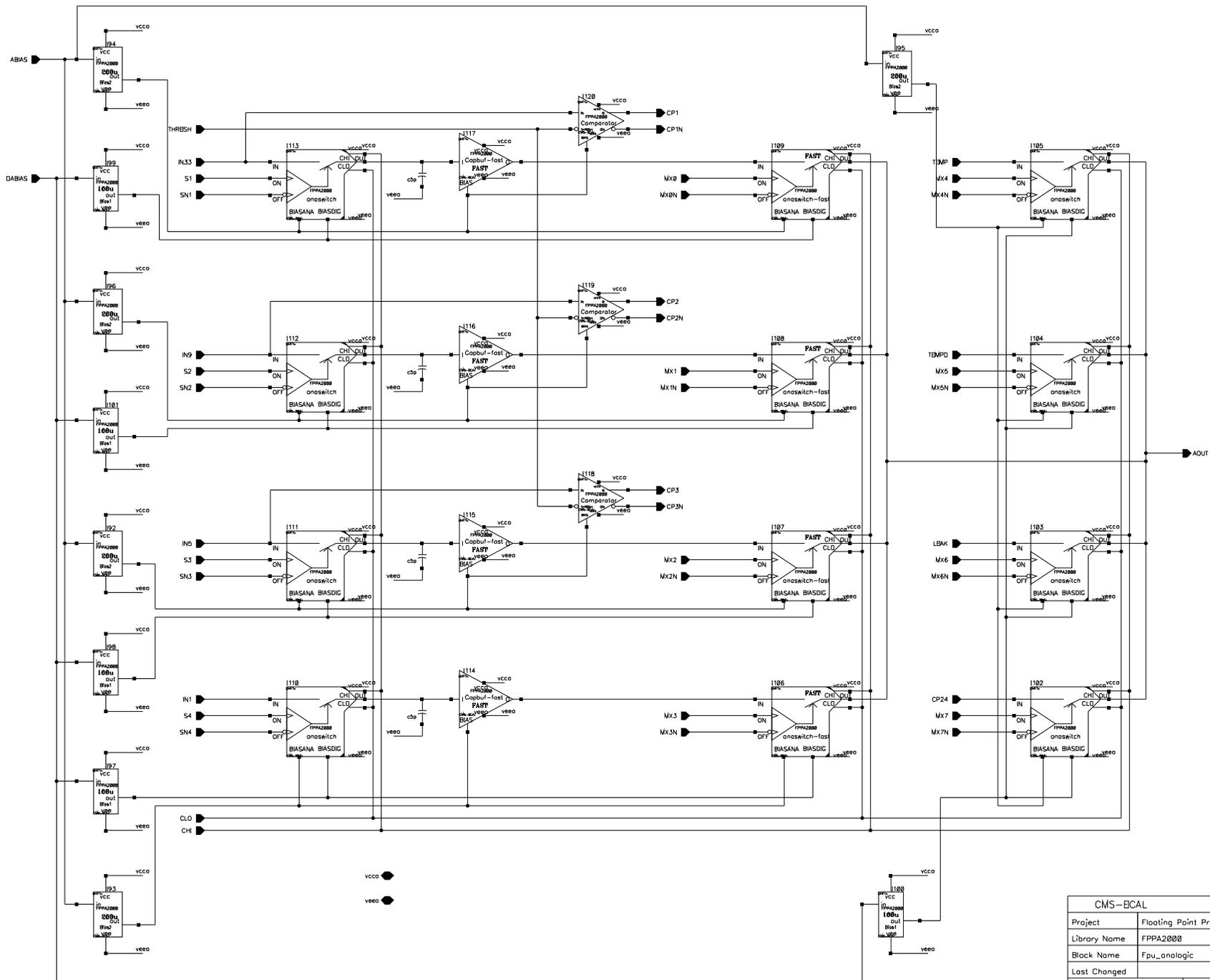
CMS-BCAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	intc -> FPPA2000/slow_cont		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



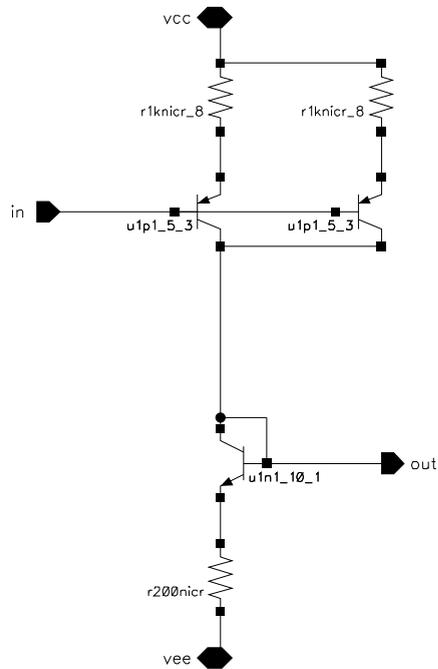
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	bias_intc -> FPPA2000/slow_cont/intc		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE

FPU_ANALOGIQUE



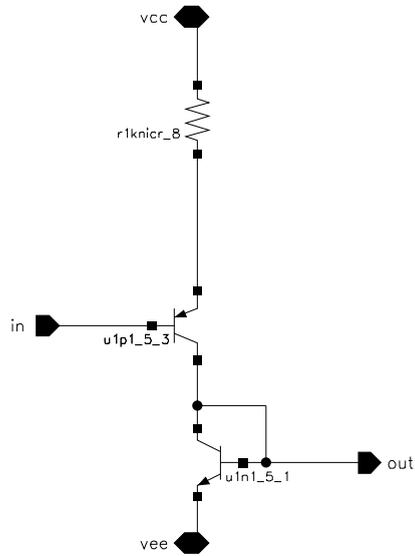


CMS-BCAL		Princeton University / IPNL	
Project	Floating Point Pre-Amplifier		
Library Name	FPPA2000		
Block Name	Fpu_analogic	-> FPPA2000	
Last Changed			
Preliminary Design Review	TRUB	Critical Design Review	TRUB



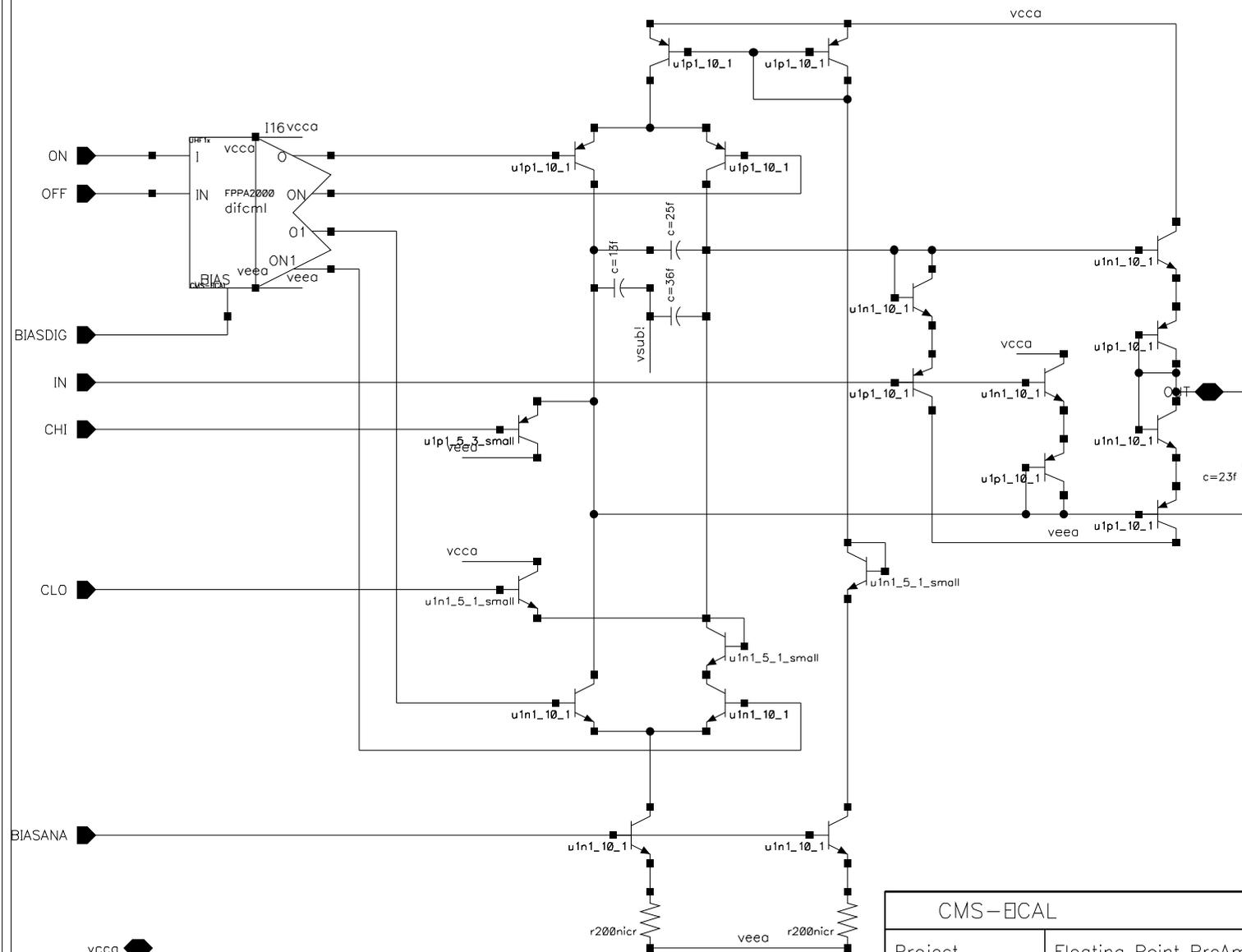
JMB – July 10, 2000 : Added R2 which was missing

CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	Bias2 -> FPPA2000/Fpu_analogic		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



JMB – July 10, 2000 : Removed R1 (which was in serie with R0)

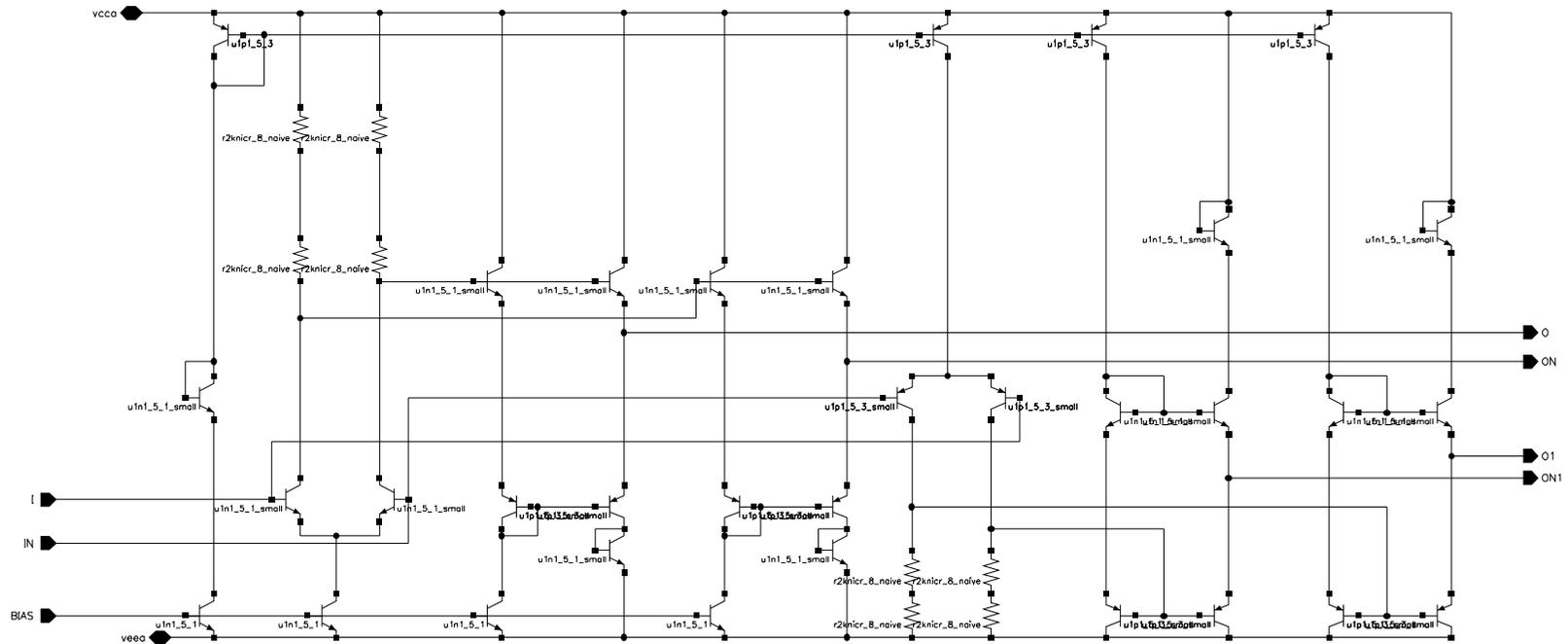
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	Bias1 -> FPPA2000/Fpu_analogic		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



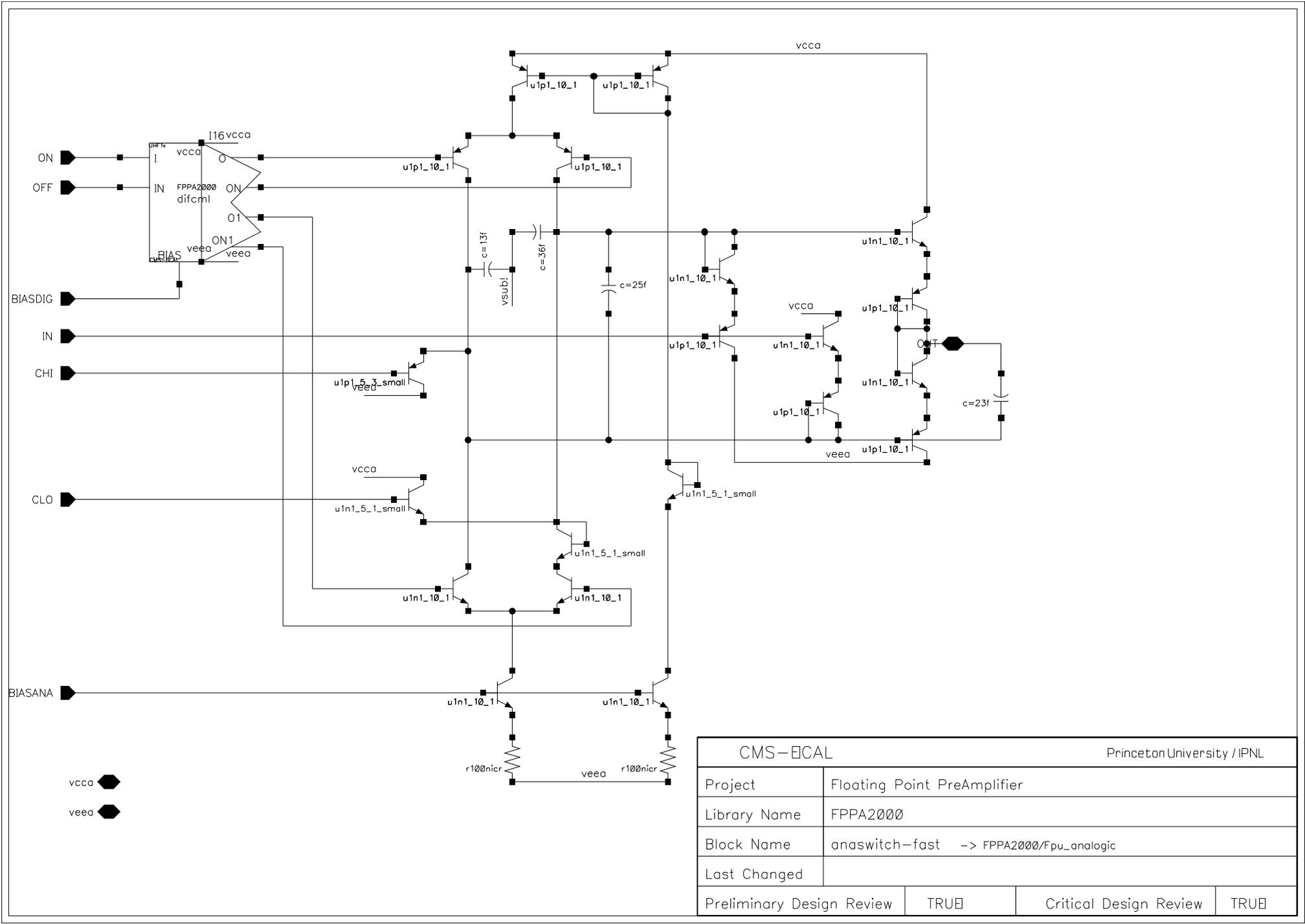
vcc

vee

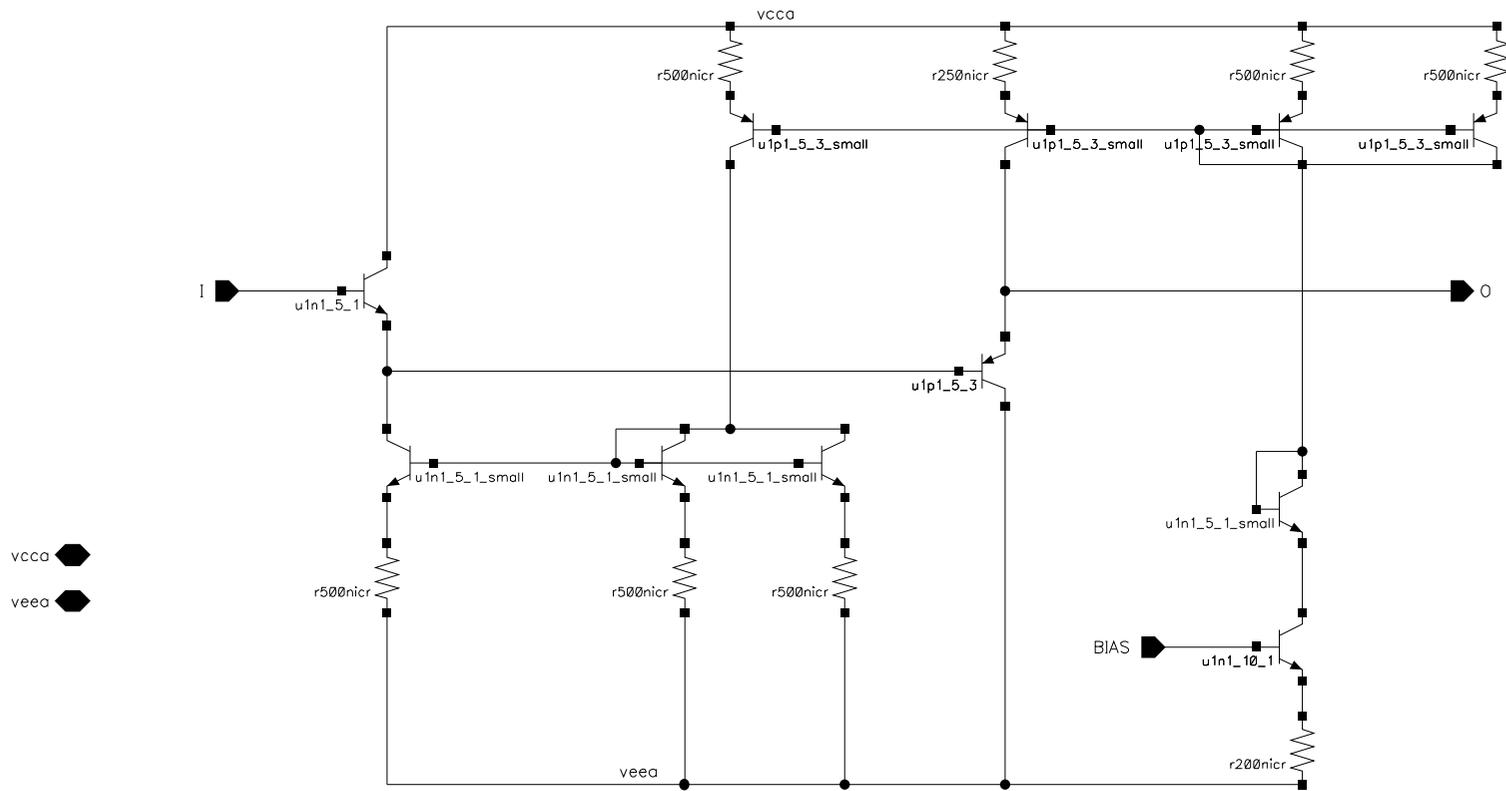
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	anaswitch -> FPPA2000/Fpu_analogic		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	d1cml -> FPPA2000/Fpu_analogic/gnawswitch FPPA2000/Fpu_analogic/gnawswitch-fast		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE

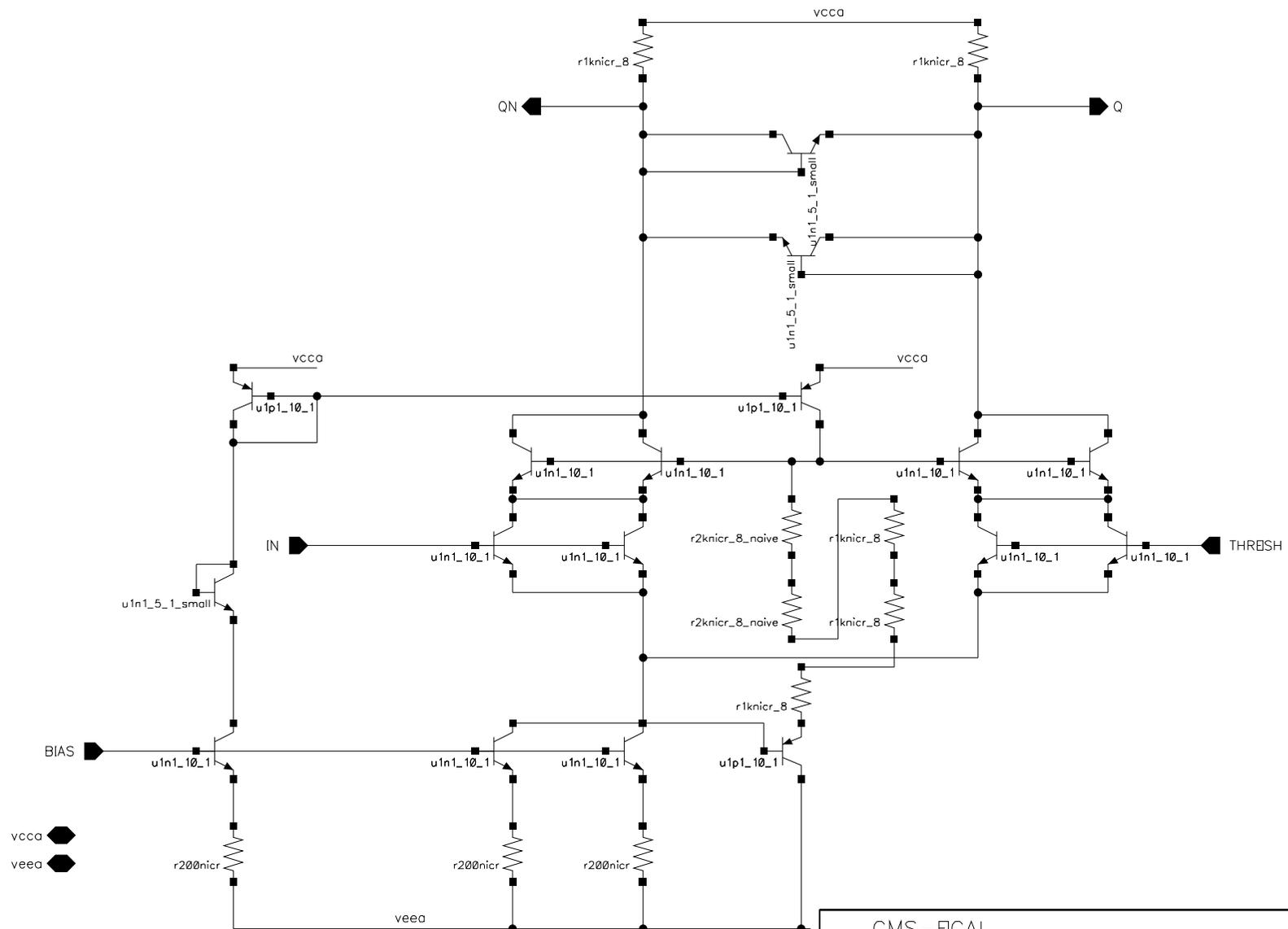


CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	anaswitch-fast -> FPPA2000/Fpu_analogic		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE

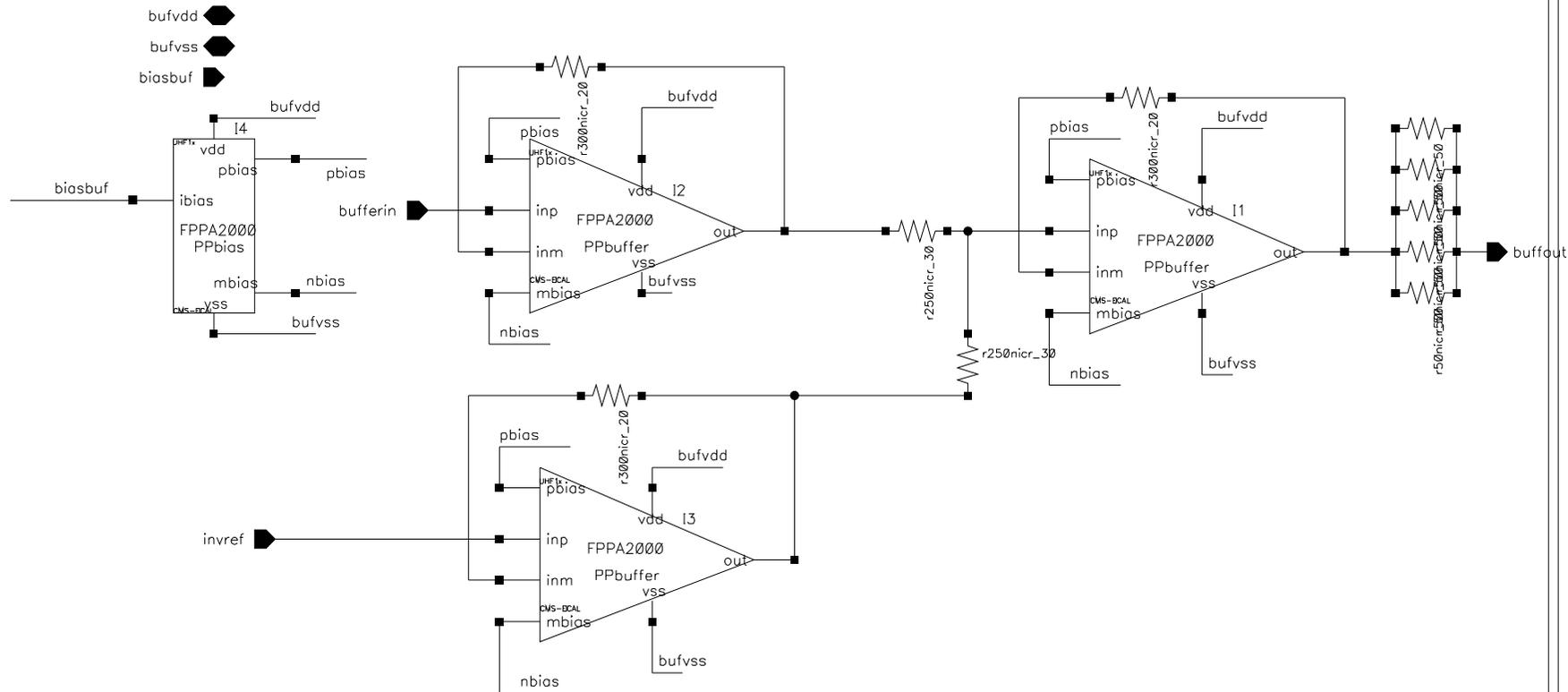


vcc0 
veea 

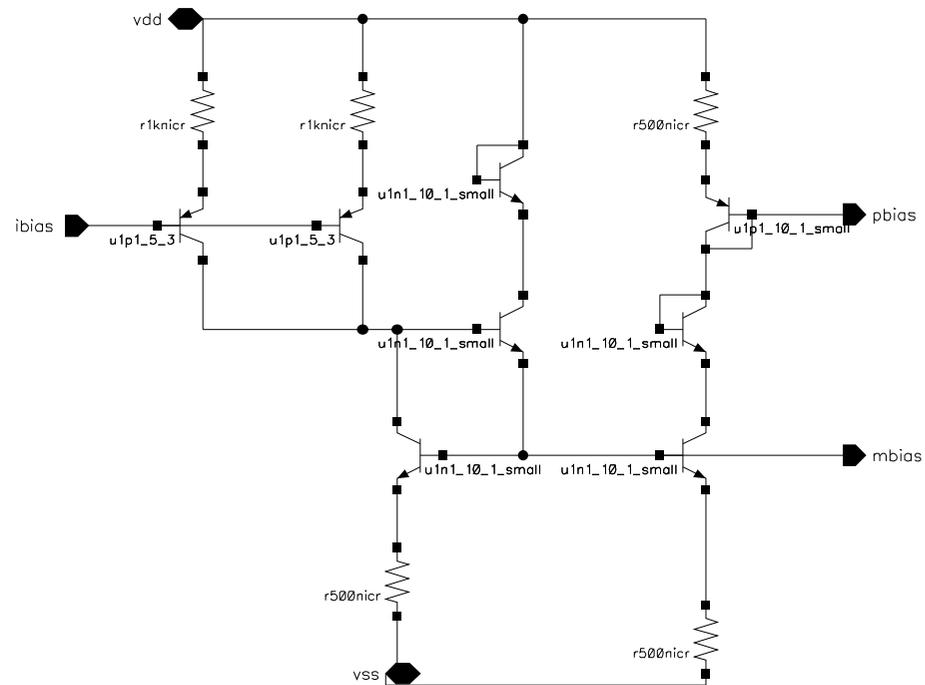
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	Capbuf-fast -> FPPA2000/Fpu_analogic		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



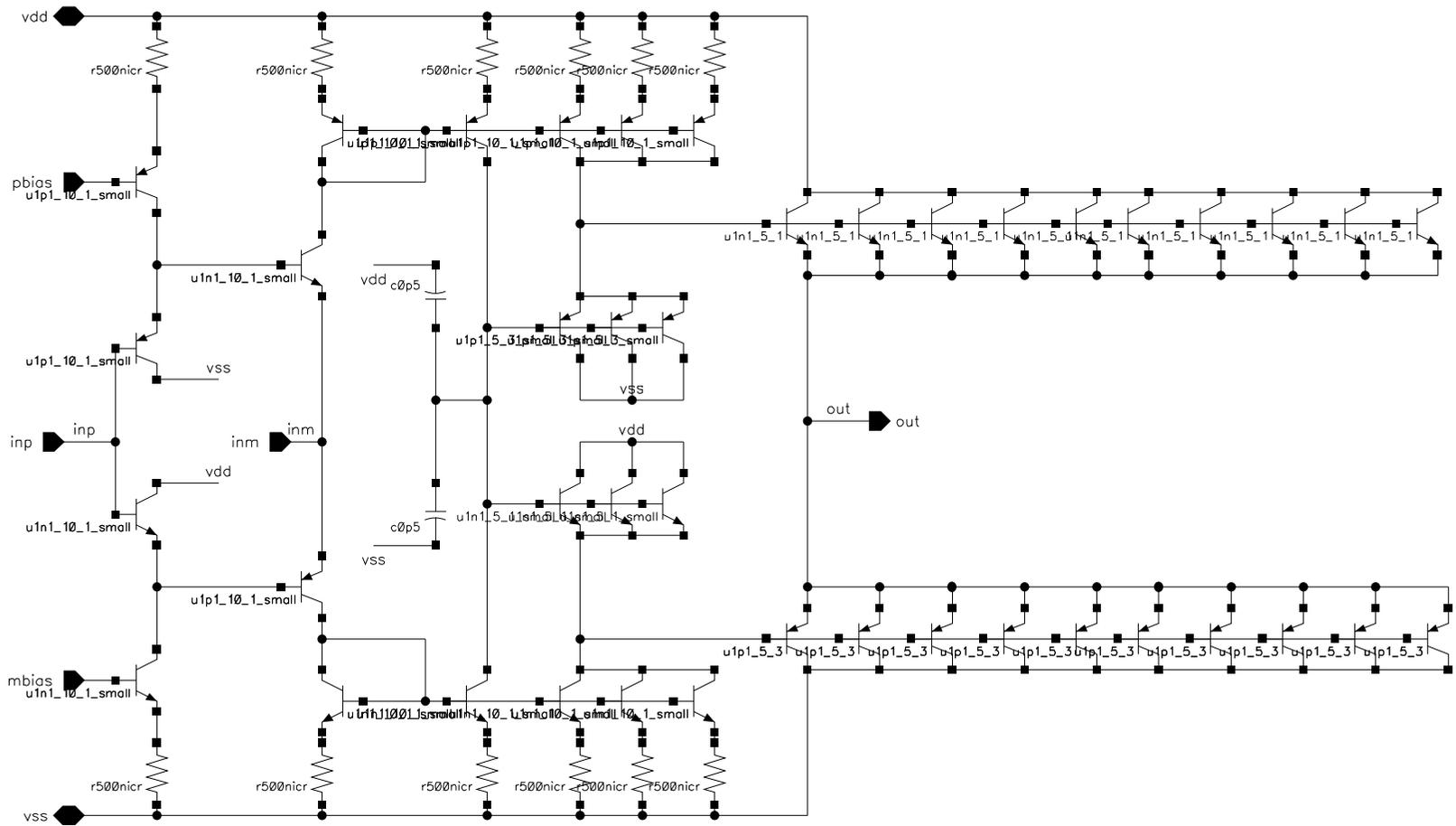
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	Comparator -> FPPA2000/Fpu_analogic		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



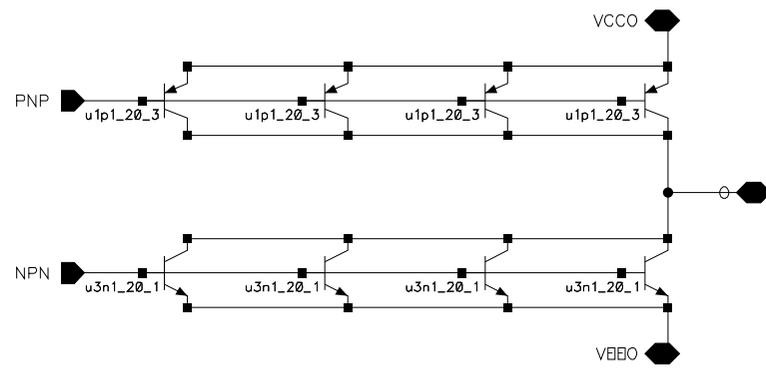
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	Buffer	-> FPPA2000	
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



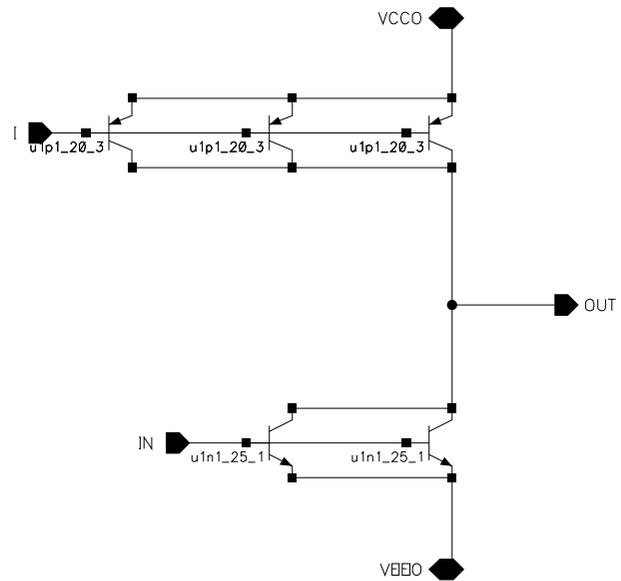
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	PPbias -> FPPA2000/Buffer		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



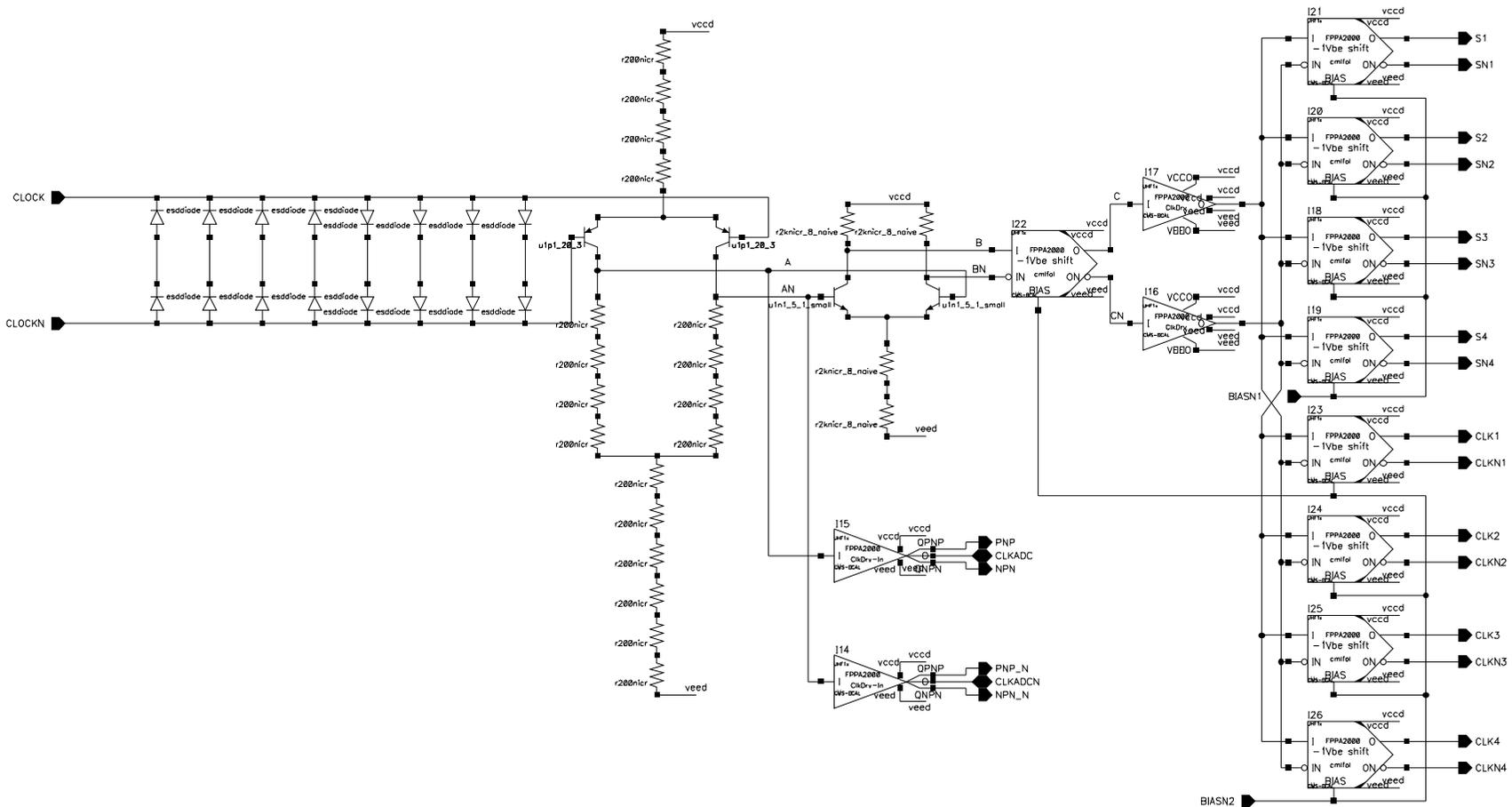
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	PPbuffer -> FPPA2000/Buffer		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	ClkDrv-Out -> FPPA2000		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



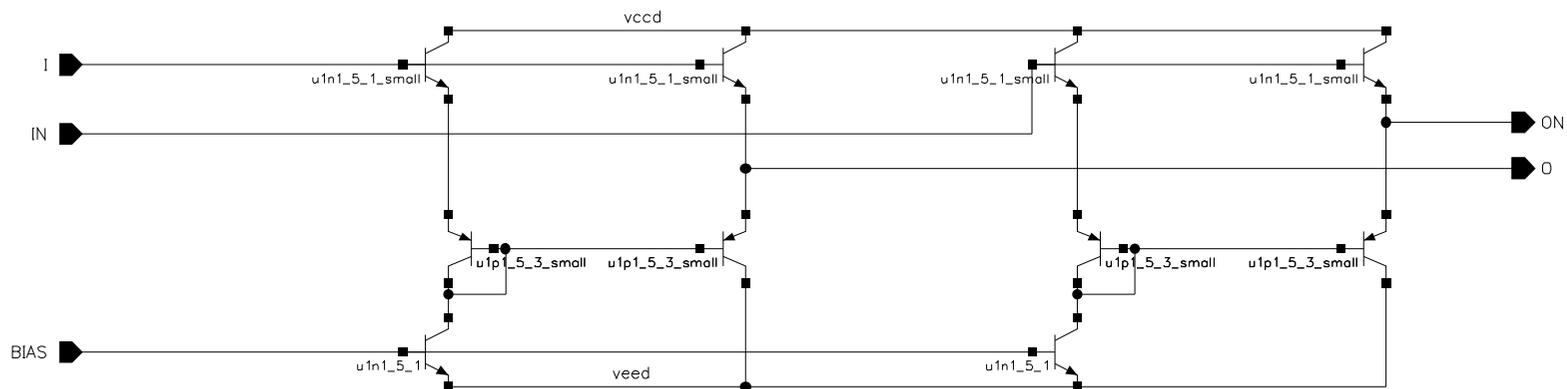
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	GaasOut-Out -> FPPA2000		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



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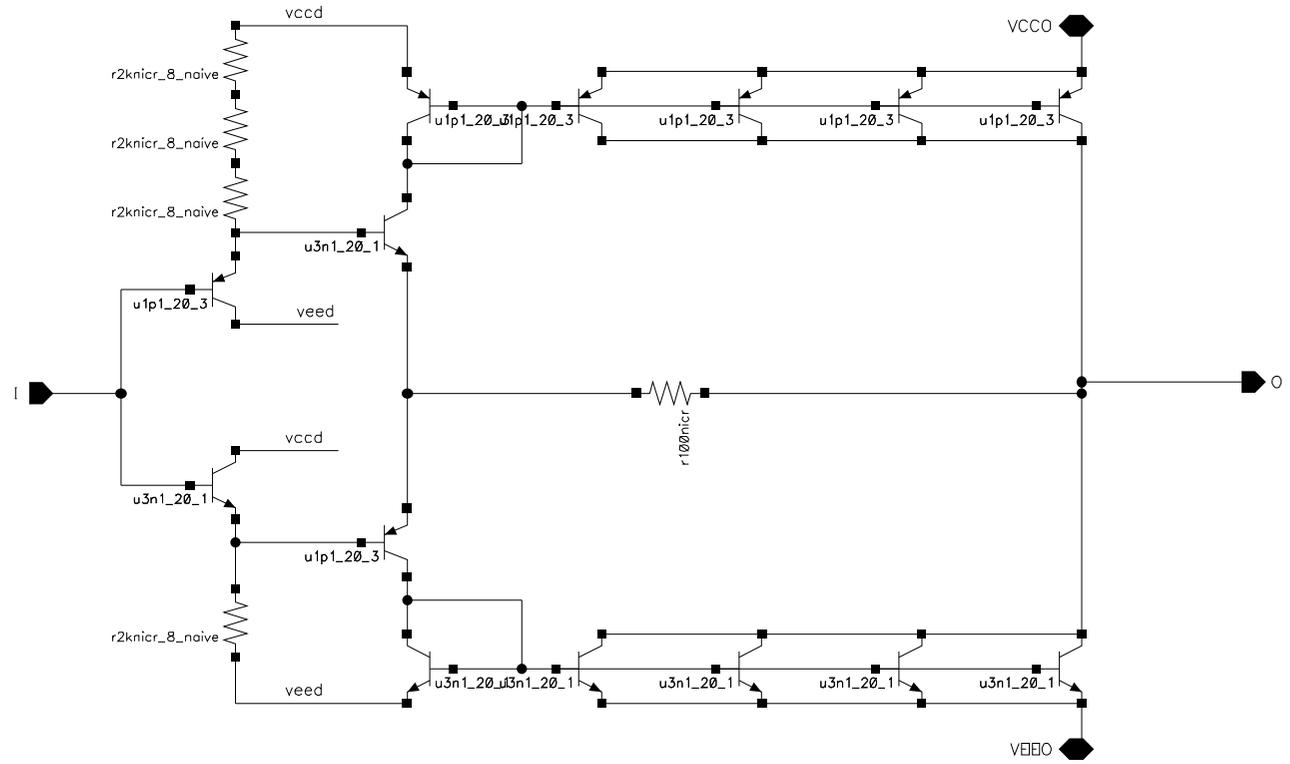
CMS-BCAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	Fpu_clock	-> FPPA2000	
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



vccd

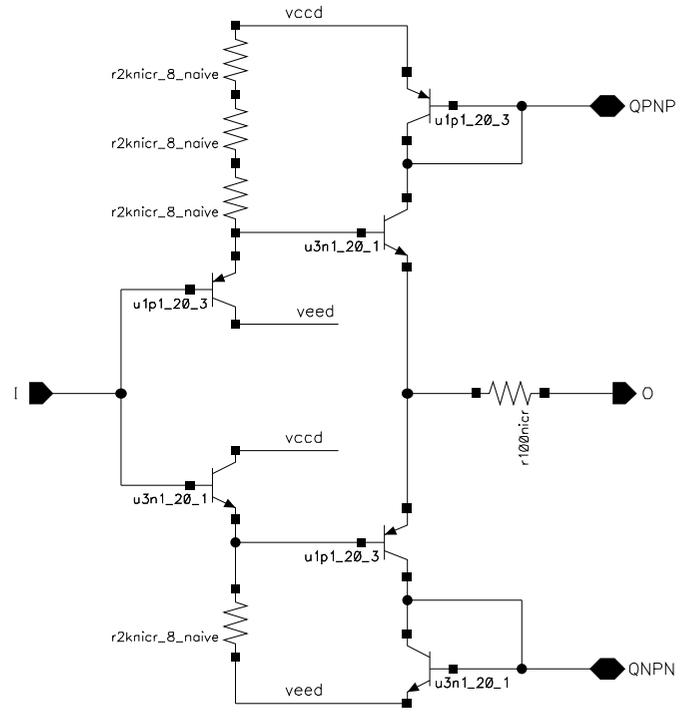
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CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	cmlfol => FPPA2000/Fpu_clock => FPPA2000/Fpu_logic		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



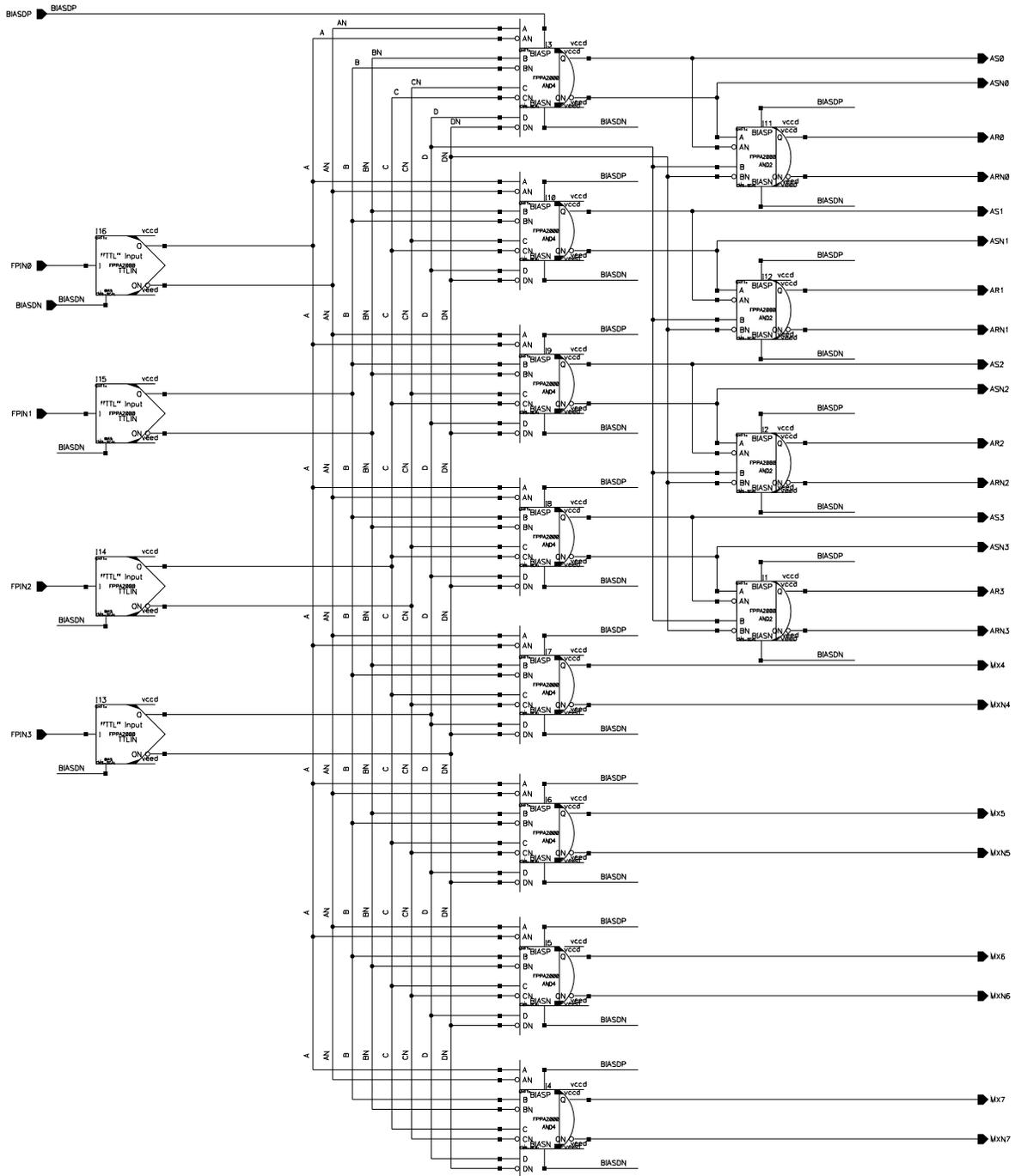
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CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	ClkDrv -> FPPA2000/Fpu_clock		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



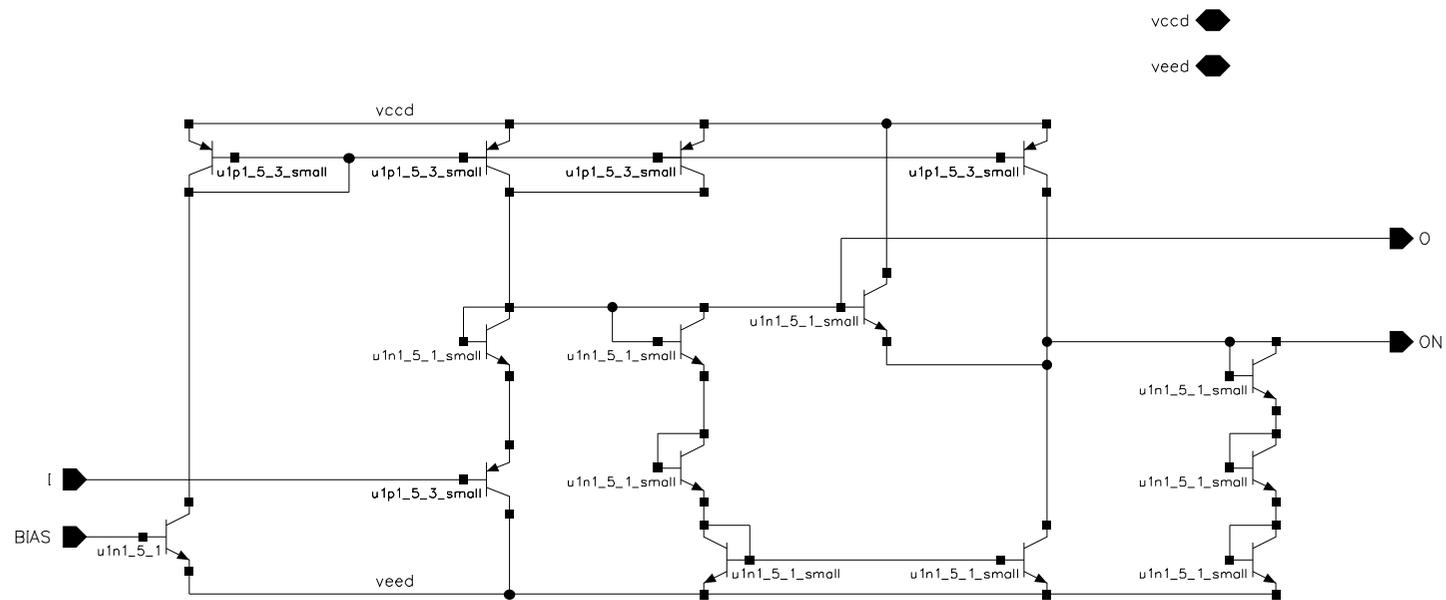
vccd 
veed 

CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	ClkDrv-In -> FPPA2000/Fpu_clock		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE

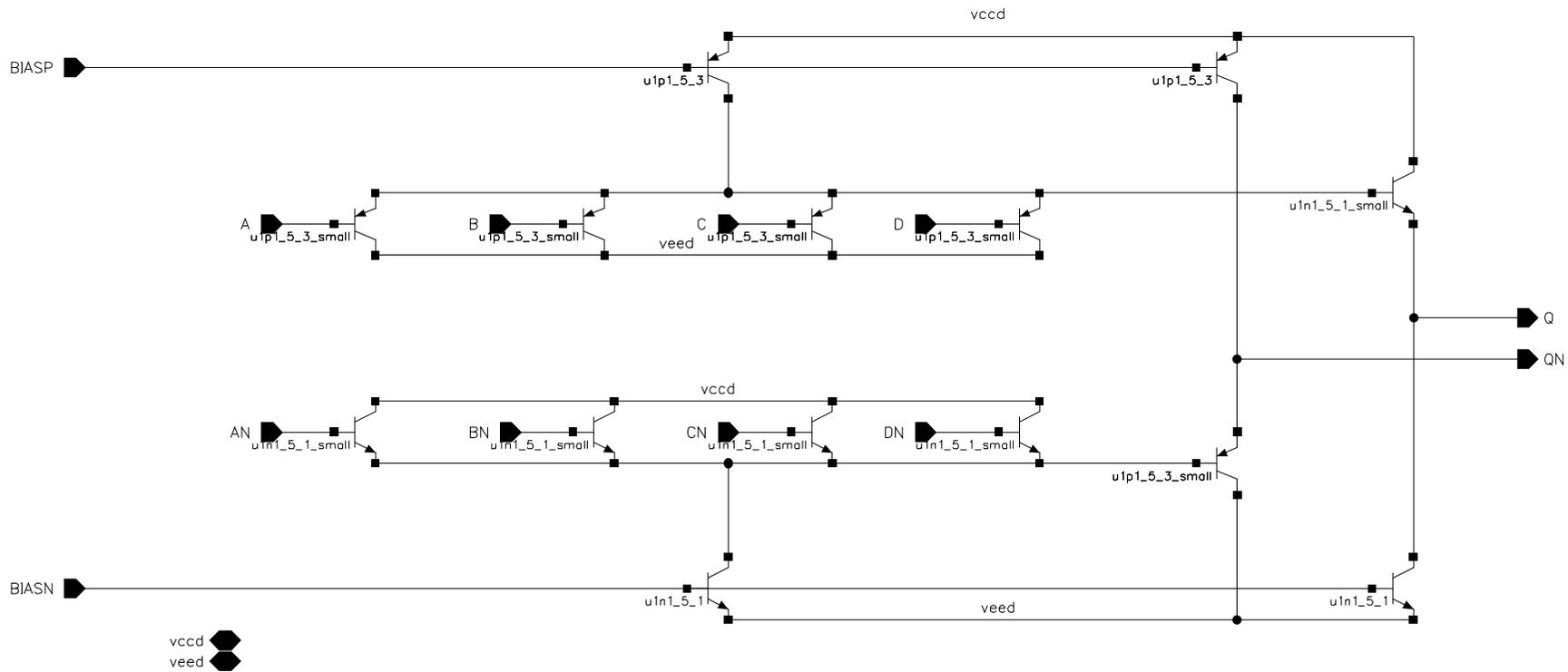


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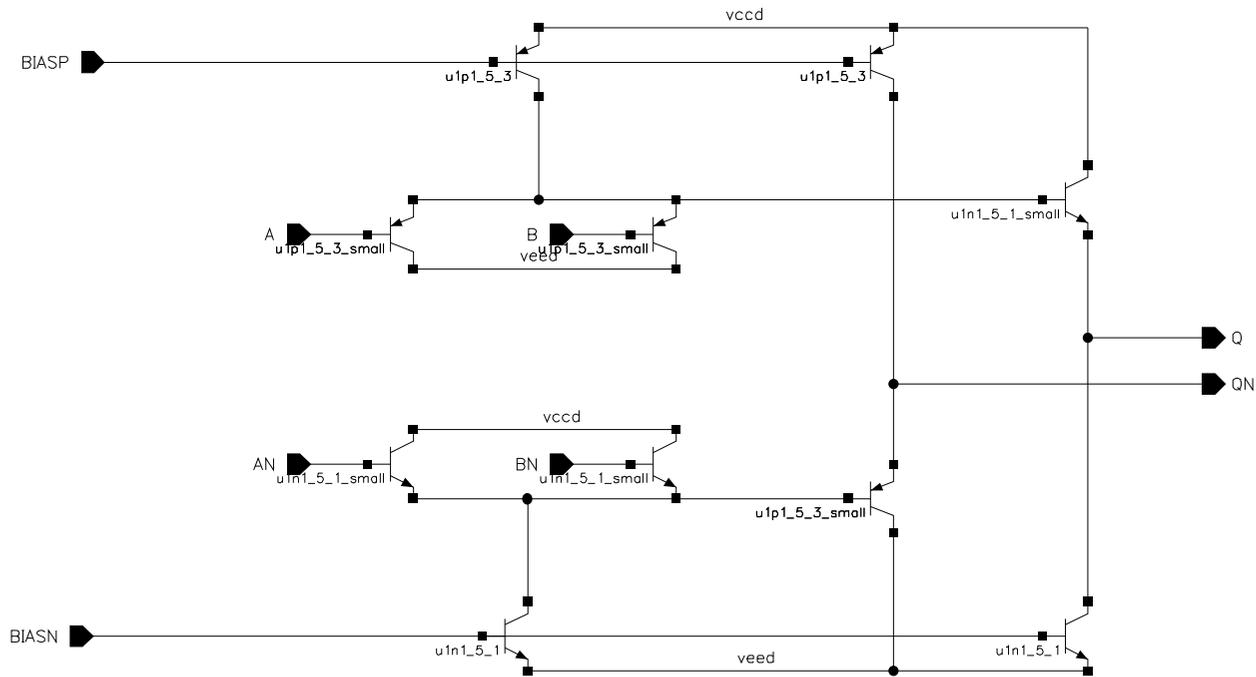
CMS-BCAL		Princeton University / IPNL	
Project	Floating Point Preamplifier		
Library Name	FPPA2000	Command Decoder	
Block Name	Fpu_control	-> FPPA2000	
Last Changed			
Preliminary Design Review	TRUB	Critical Design Review	TRUB



CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	TTLIN -> FPPA2000/Fpu_control		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE

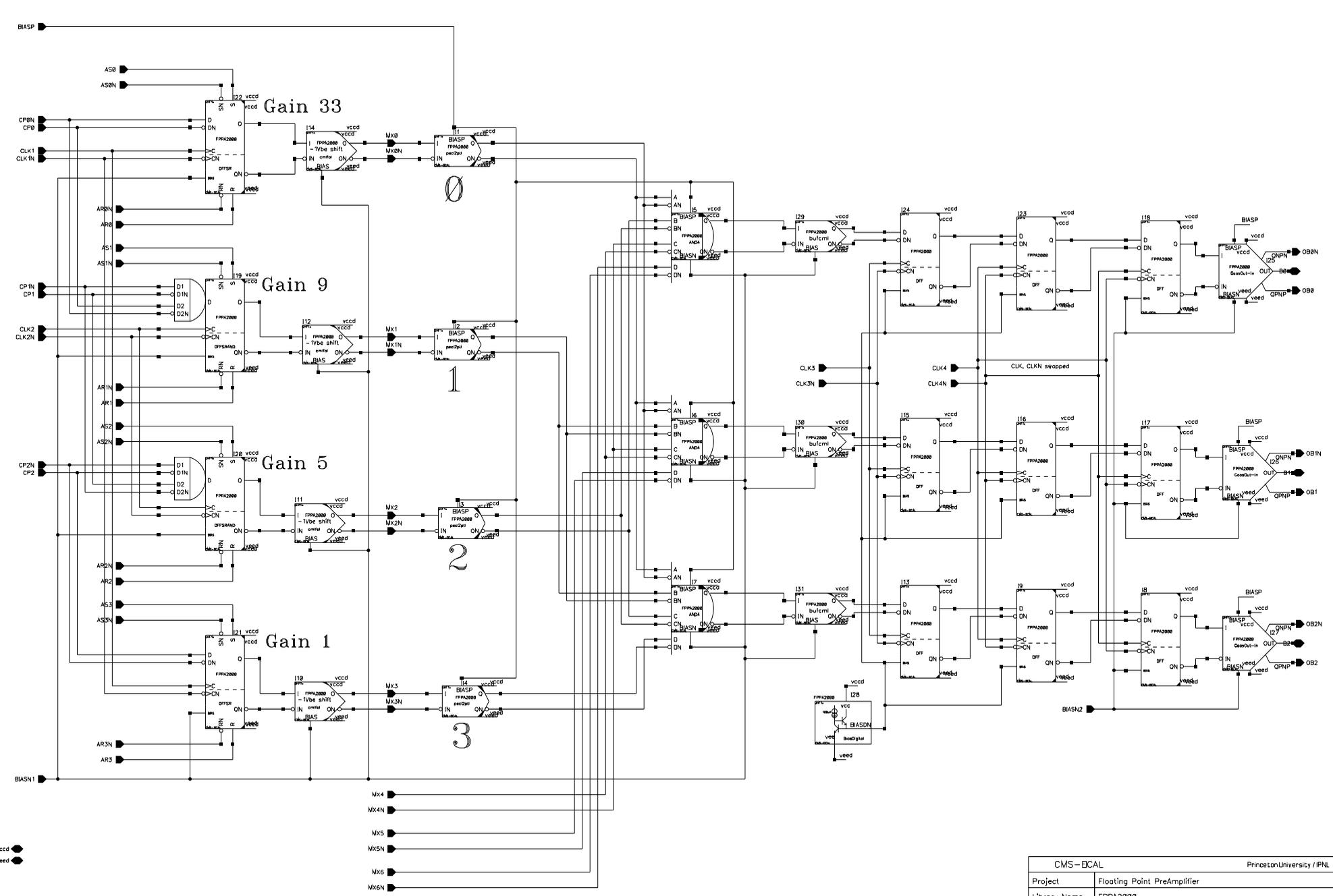


CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	AND4 => FPPA2000/Fpu_control => FPPA2000/Fpu_logic		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE

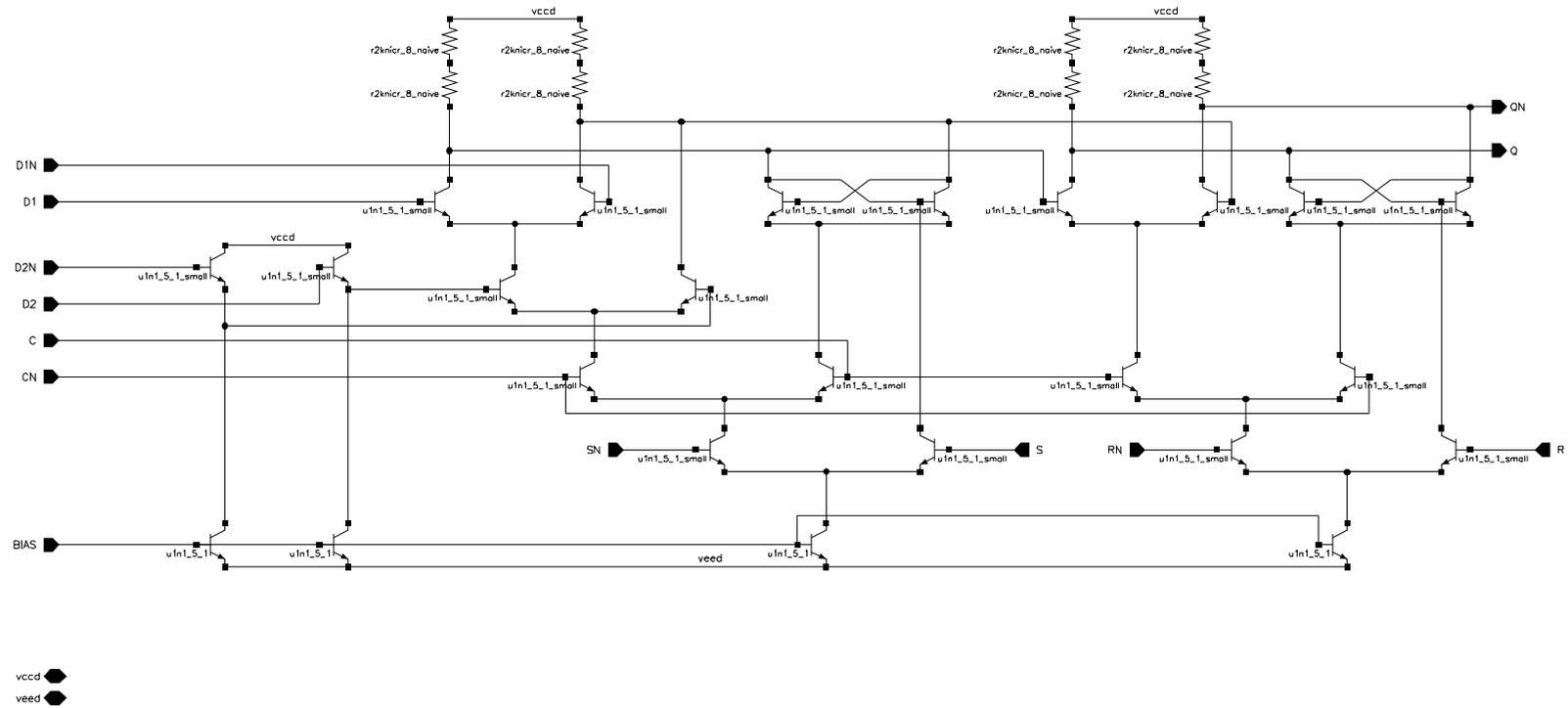


vccd 
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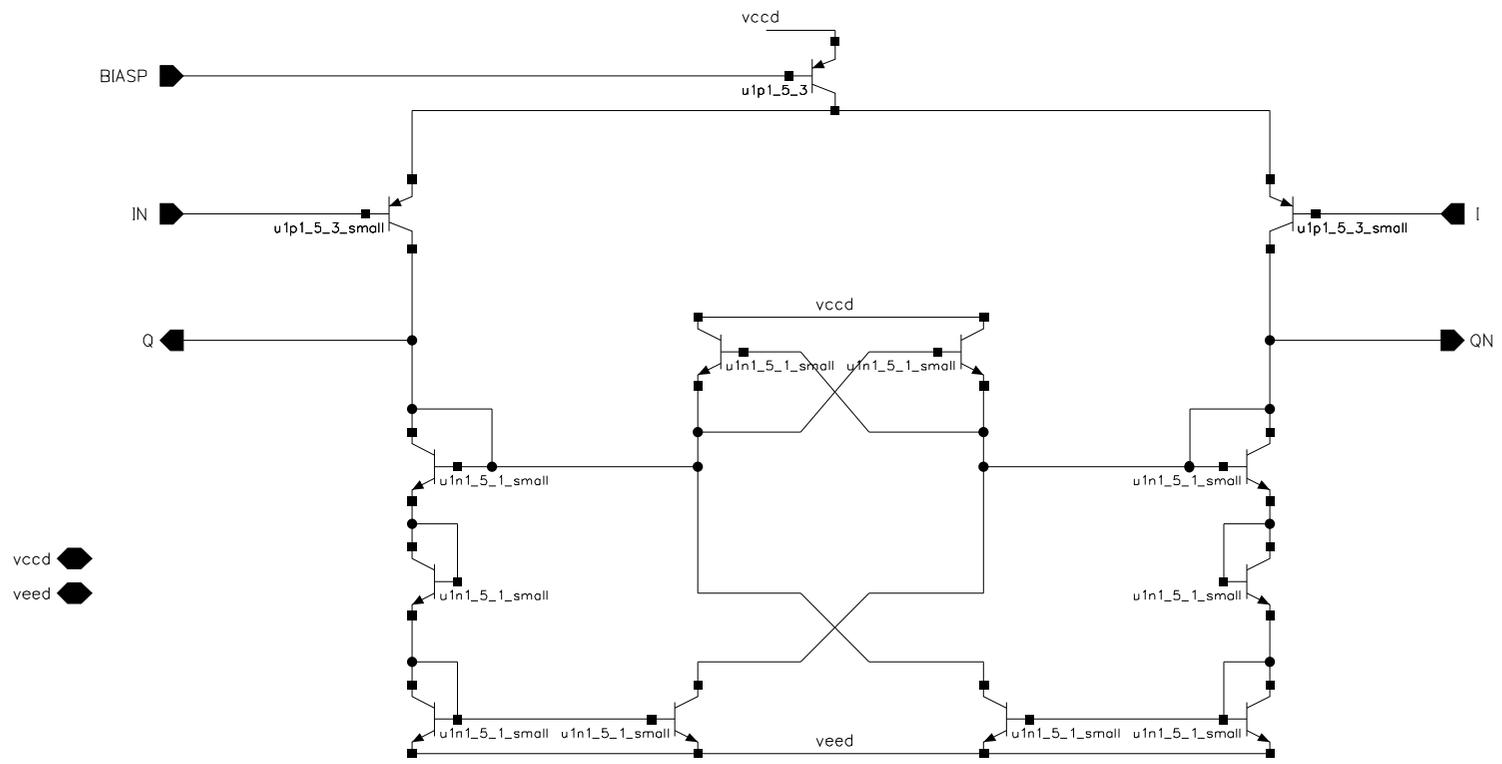
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	AND2 -> FPPA2000/Fpu_control		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



CMS-BCAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	Fpu_logic	-> FPPA2000	
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE

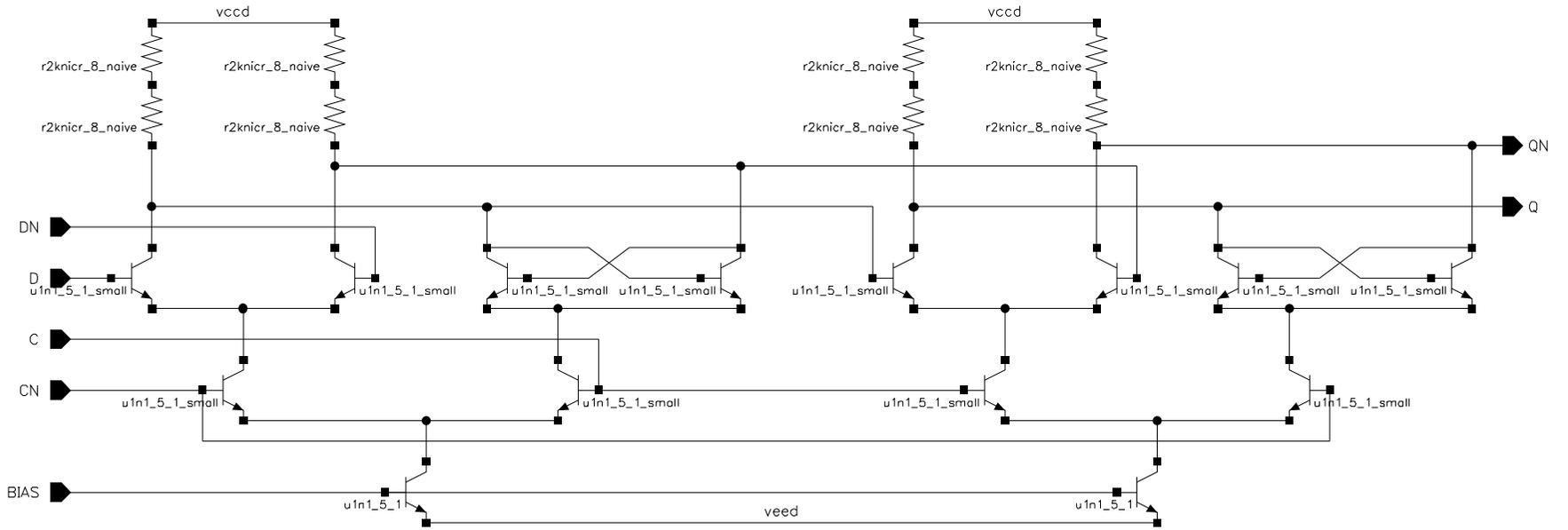


CMS-BCAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	DFFSRAND --> FPPA2000/Fpu_logic		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



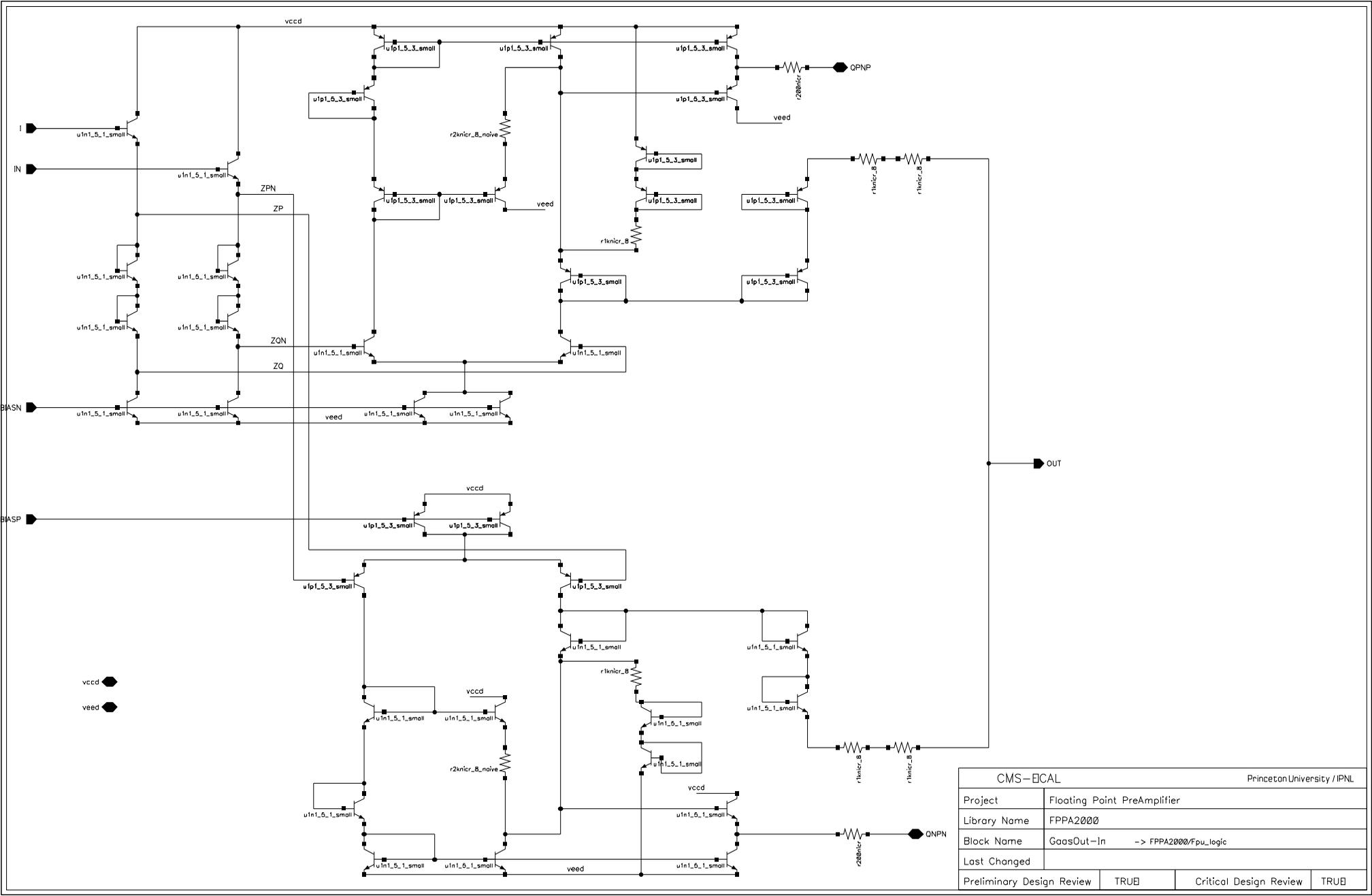
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Project	Floating Point PreAmplifier		
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Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



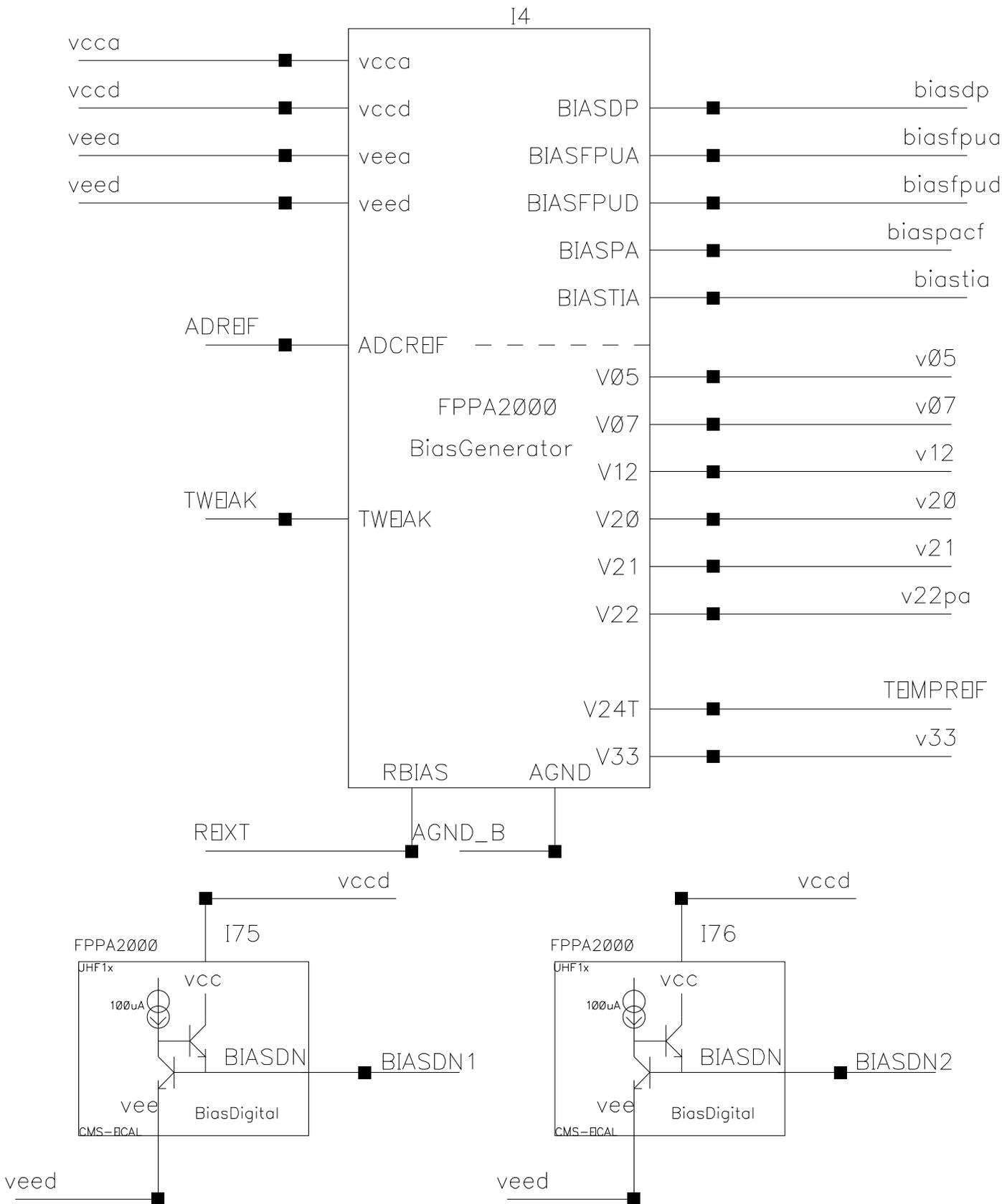
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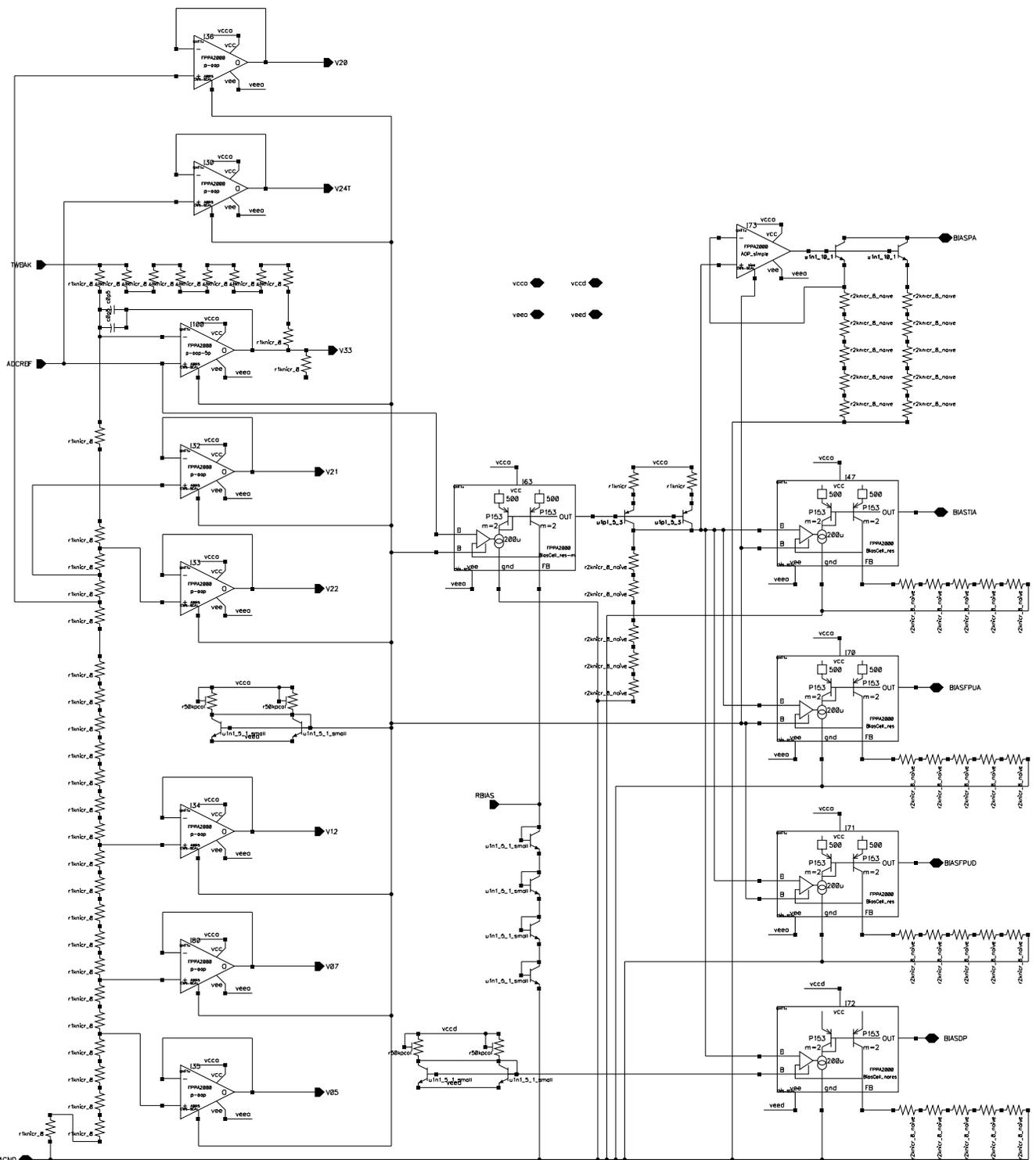
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Project	Floating Point PreAmplifier		
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Block Name	DFF -> FPPA2000/Fpu_logic		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



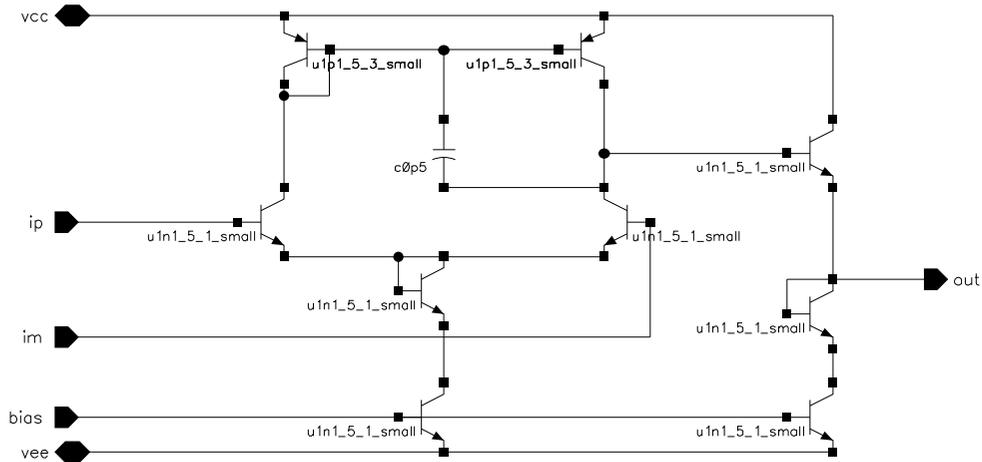
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Last Changed			
Preliminary Design Review	TRUB	Critical Design Review	TRUB

BIAS GENERATOR

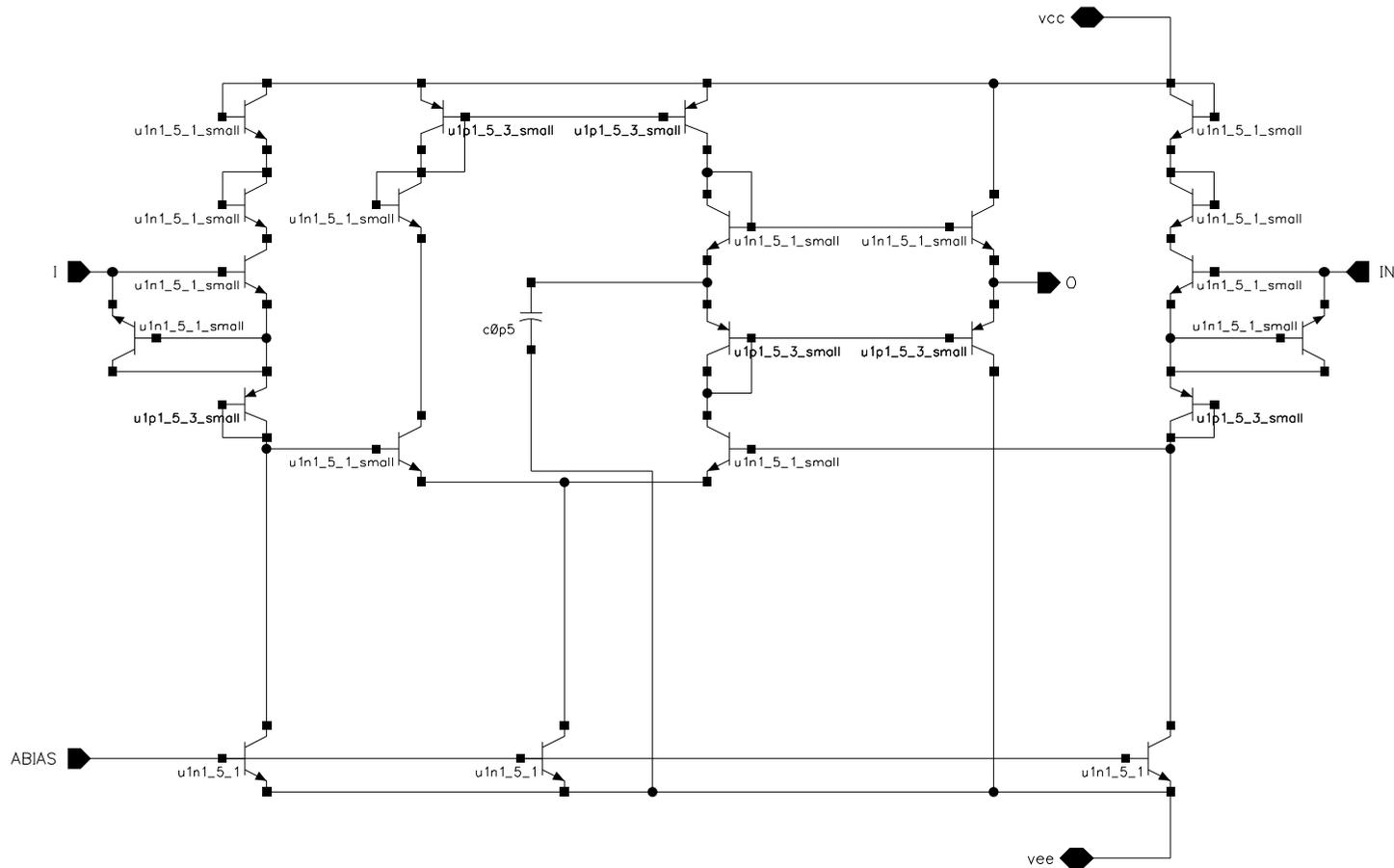




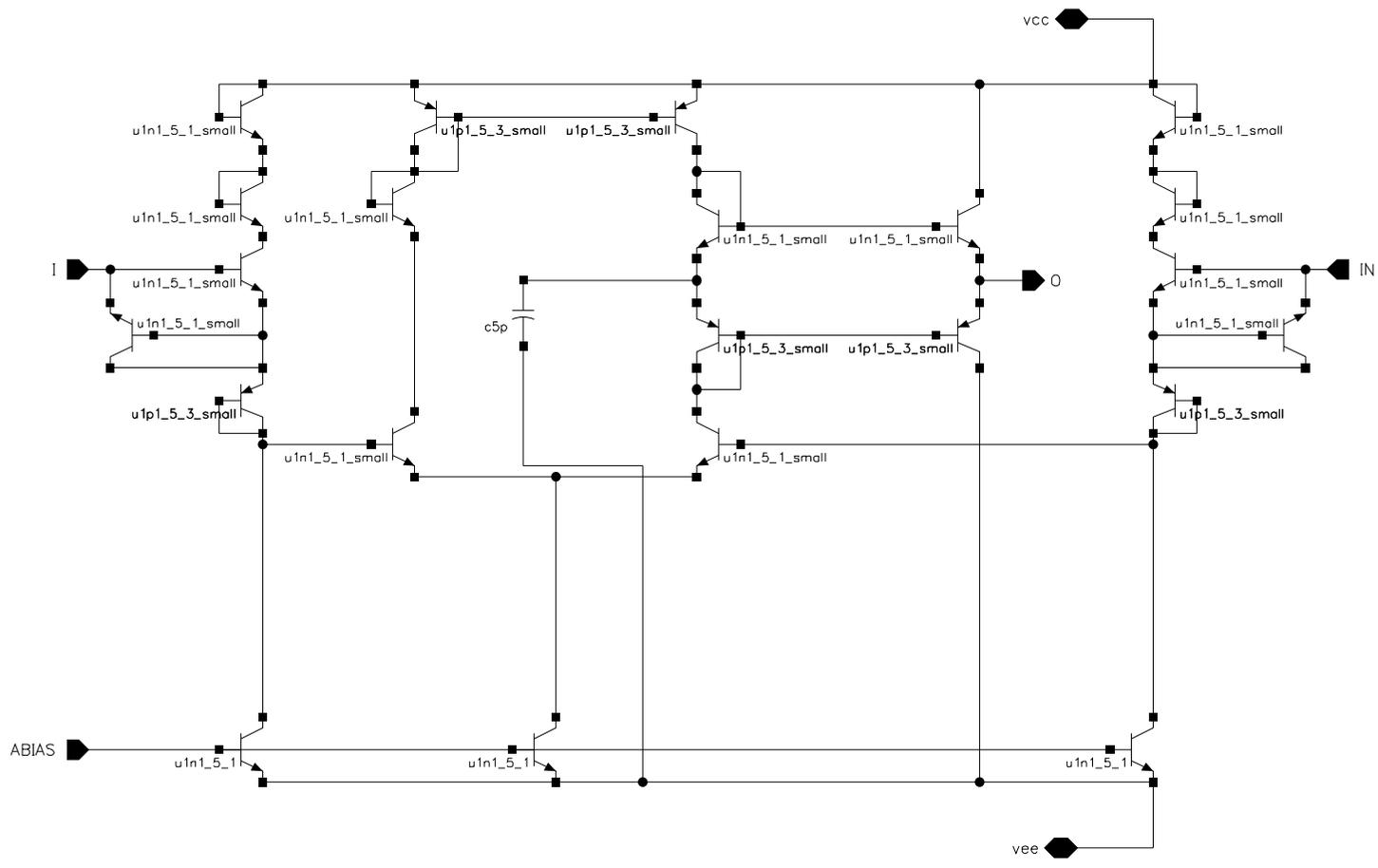
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Project	Floating Point Preamplifier	
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Last Changed		
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		TRUB



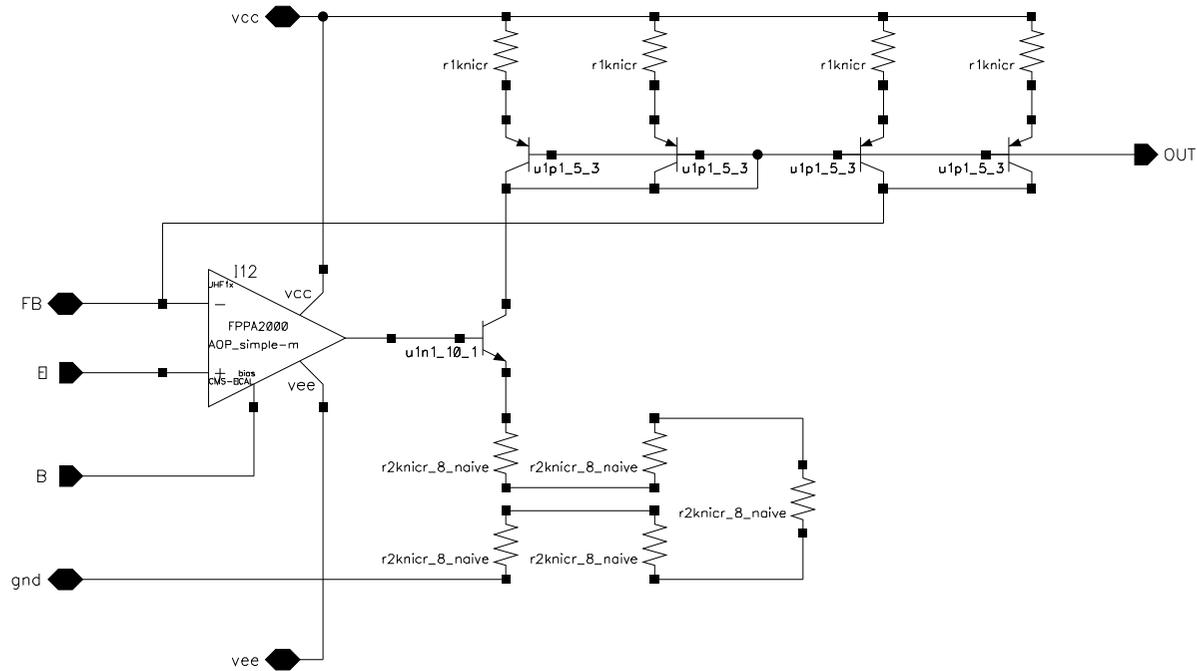
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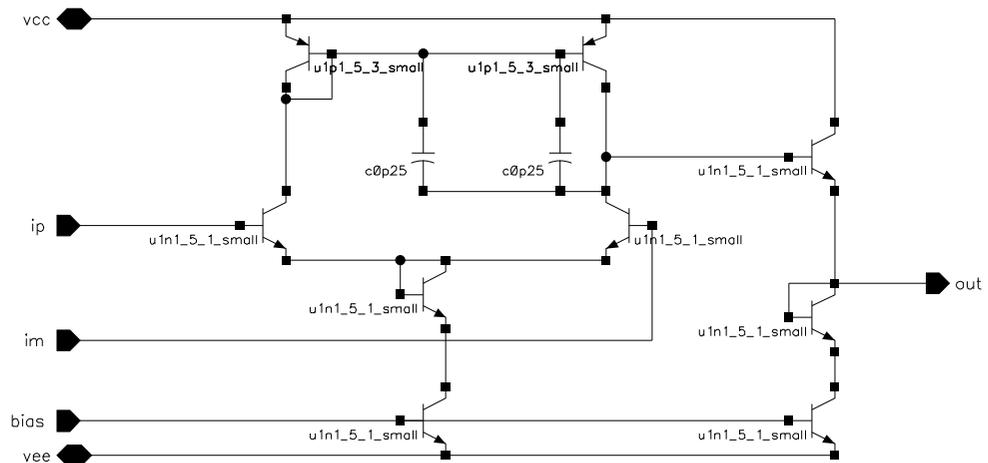
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Last Changed			
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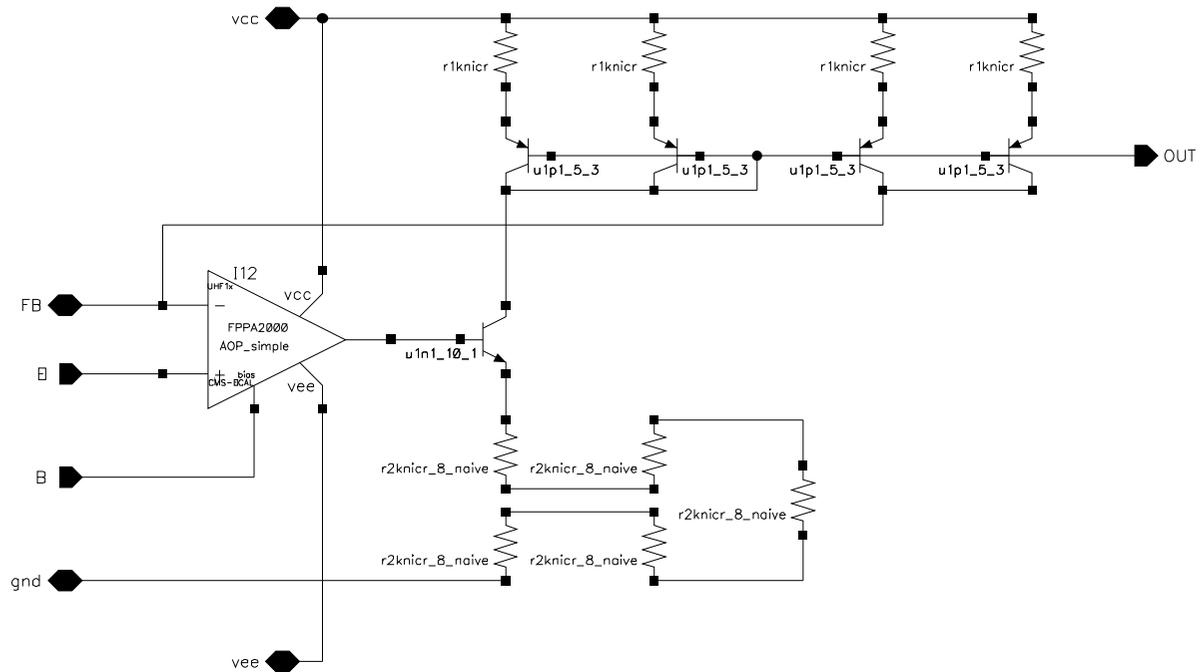
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Project	Floating Point PreAmplifier		
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Block Name	p-aop-5p -> FPPA2000/BiasGenerator		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



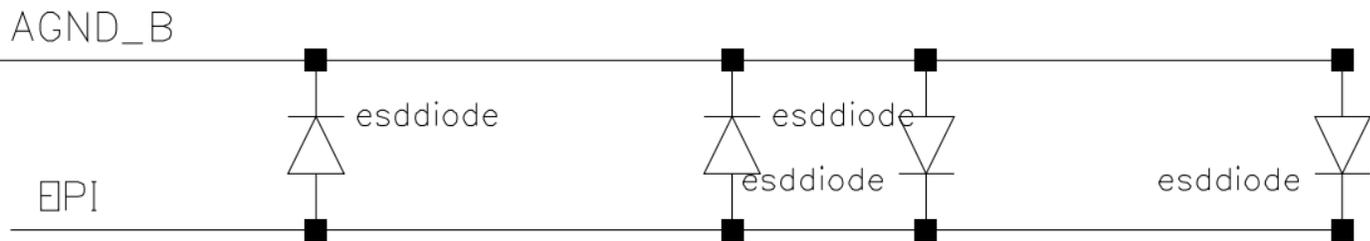
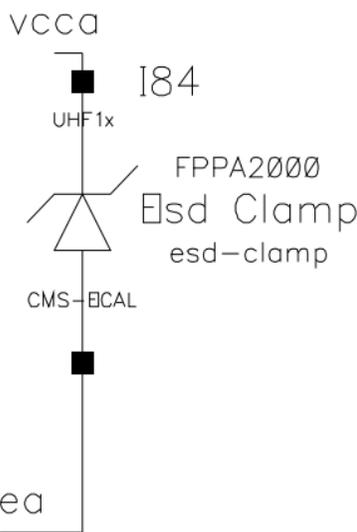
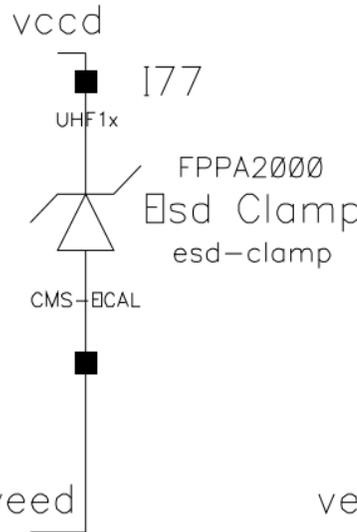
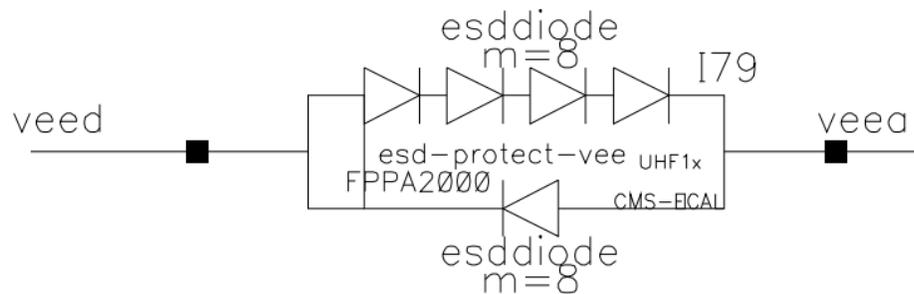
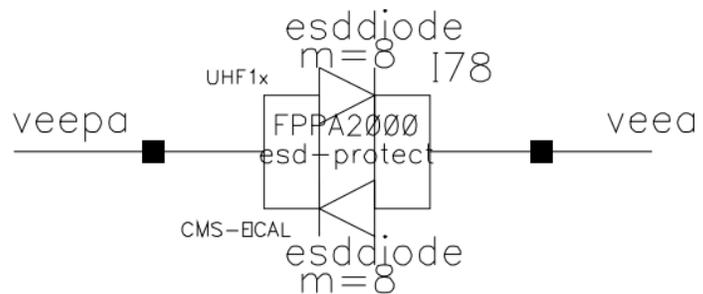
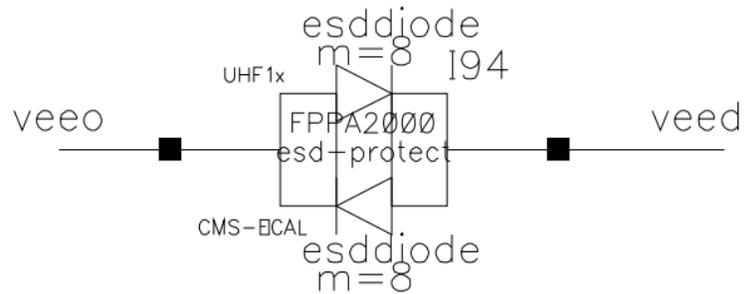
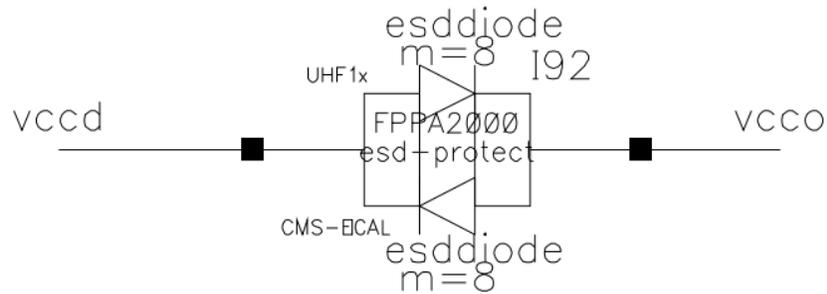
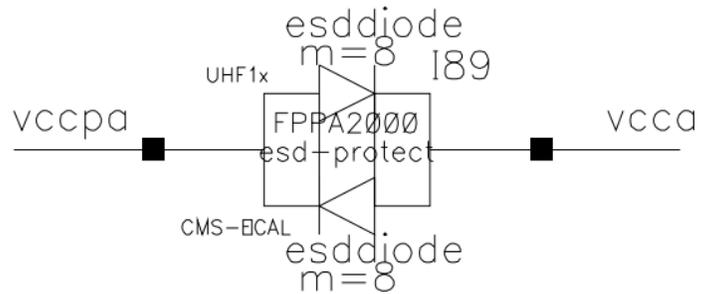
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Project	Floating Point PreAmplifier		
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Block Name	BiasCell_res-m -> FPPA2000/BiasGenerator		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE

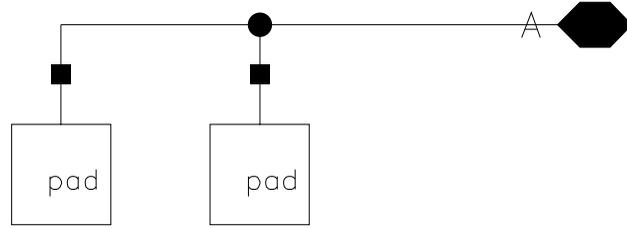


CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	AOP_simple-m -> FPPA2000/BiasGenerator/BiasCell_res-m		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	BiasCell_res -> FPPA2000/BiasGenerator		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE

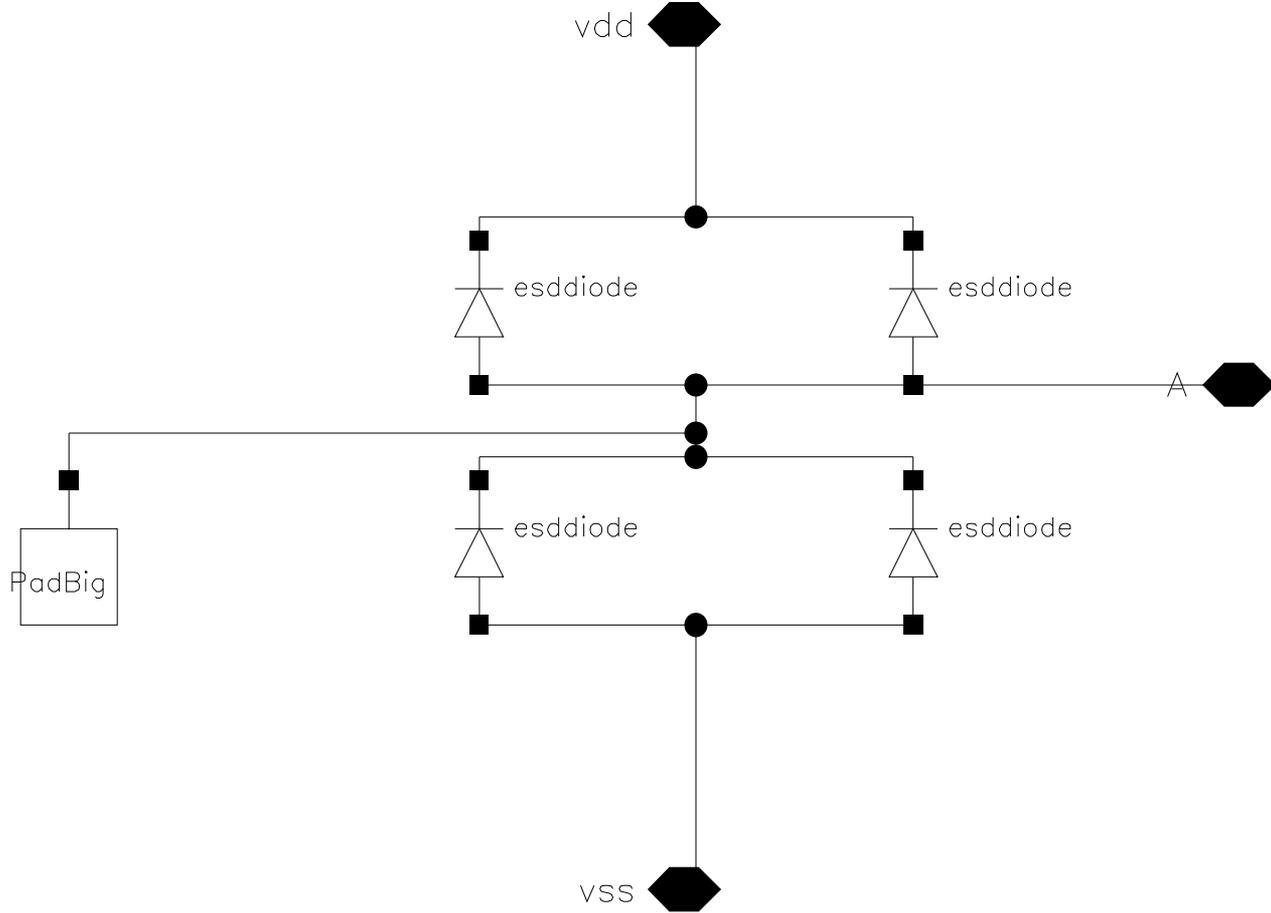




CMS – ECAL

Princeton University / IPNL

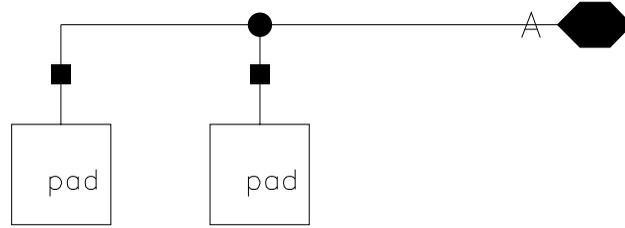
Project	Floating Point PreAmplifier		
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Block Name	ProPadPOS	-> FPPA2000	
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



CMS-ECAL

Princeton University / IPNL

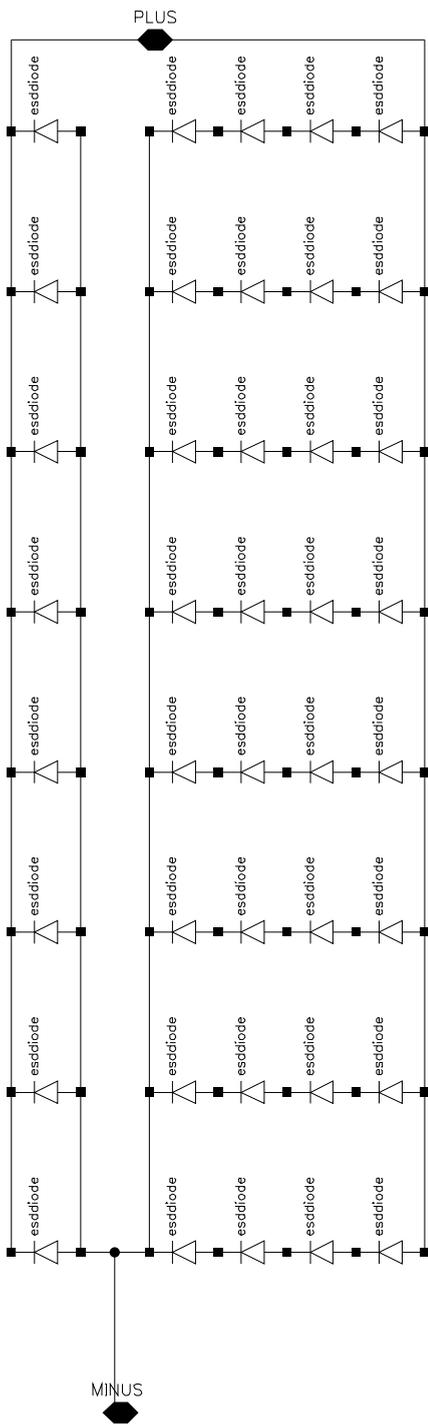
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Block Name	ProPad	-> FPPA2000	
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



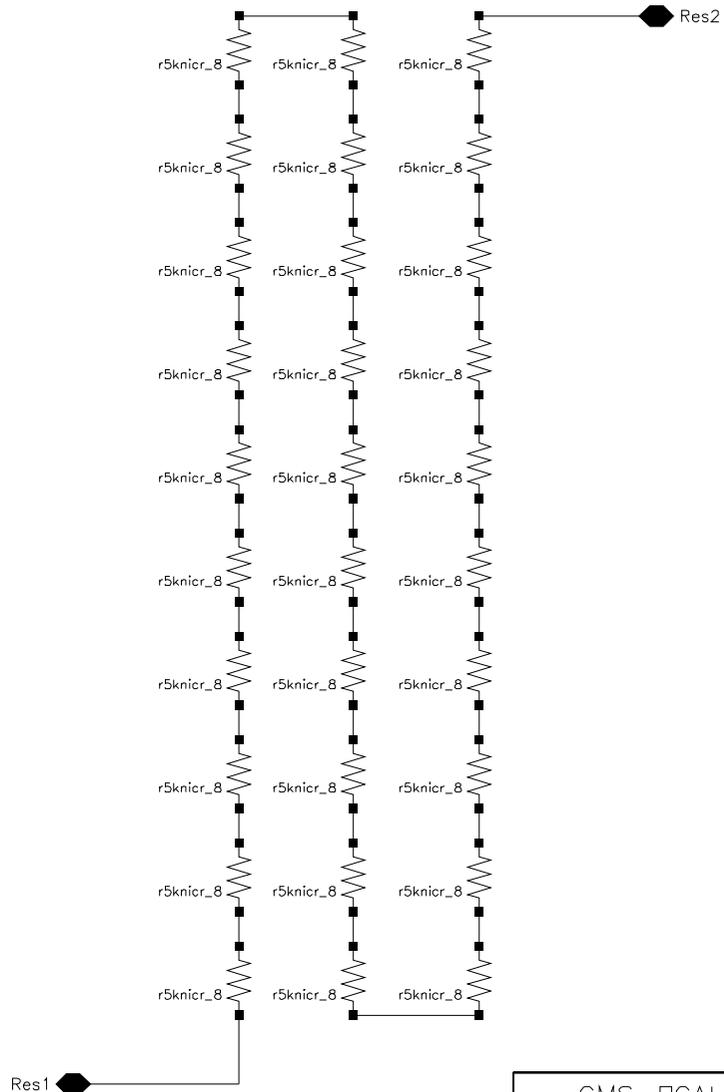
CMS-ECAL

Princeton University / IPNL

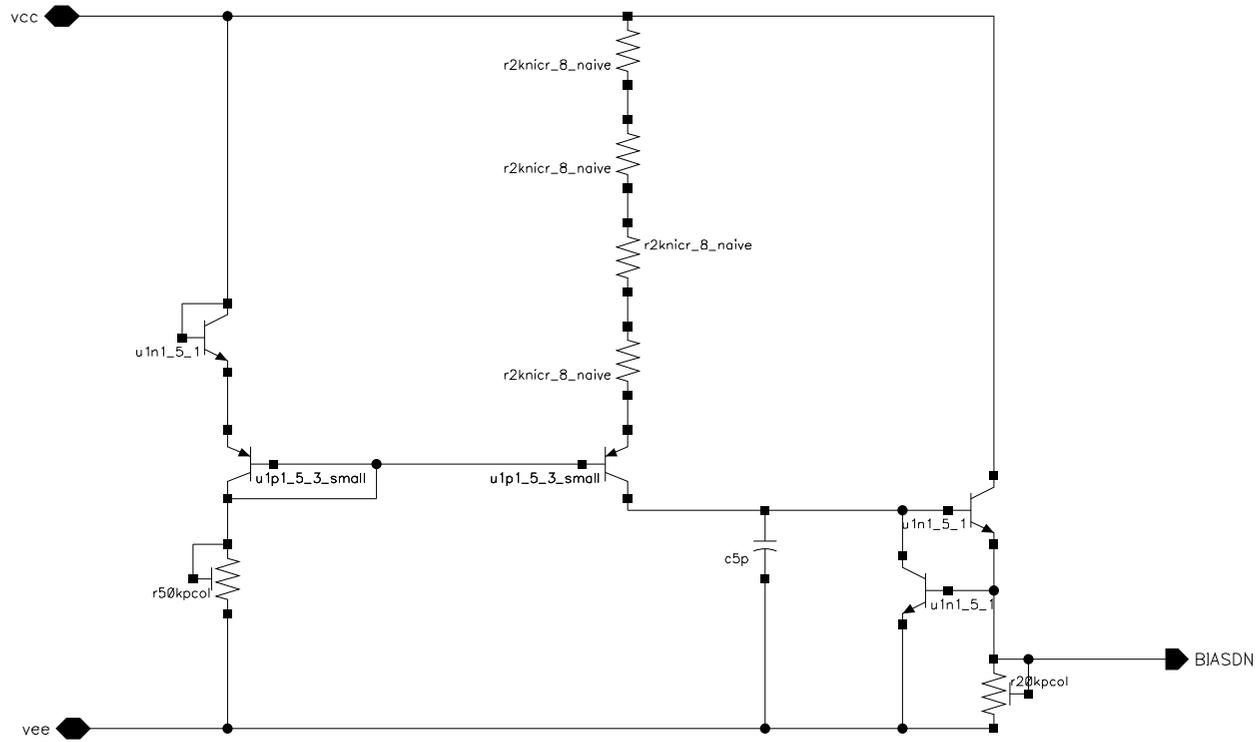
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	ProPadNEG -> FPPA2000		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



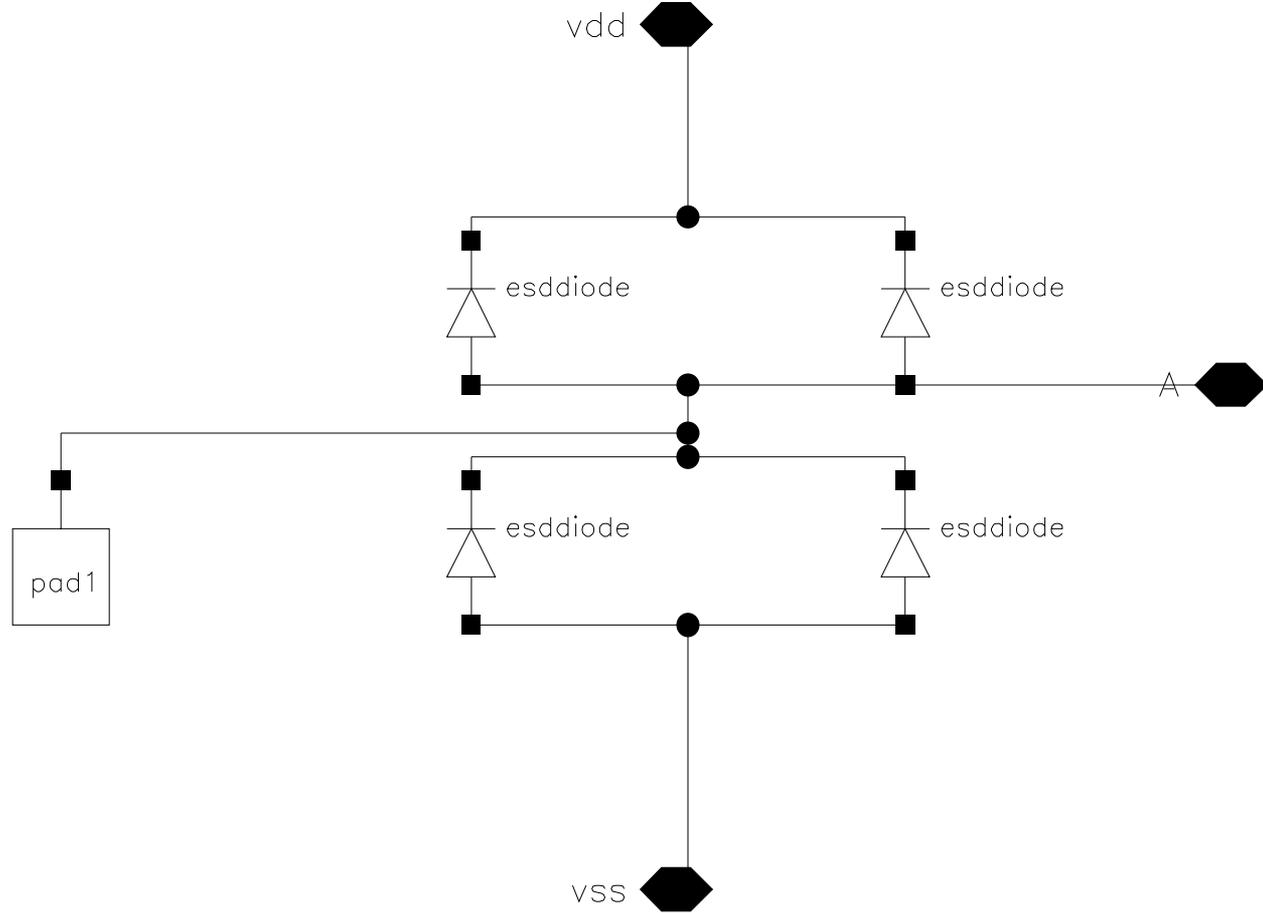
CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	esd-protect-vee	-> FPPA2000	
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	Rdummy-150k -> FPPA2000		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



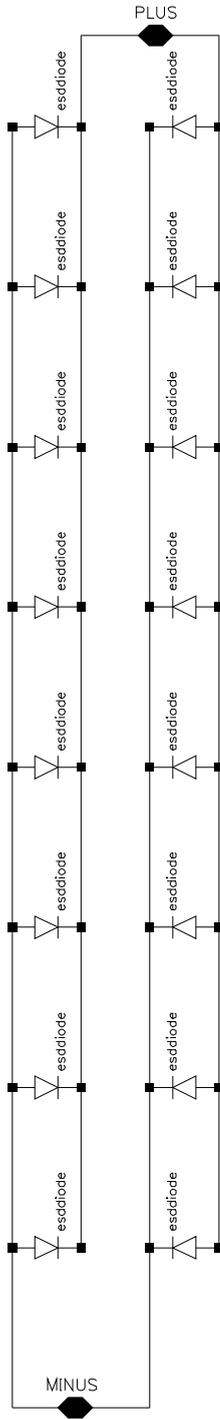
CMS-ECAL		Princeton University / IPNL	
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Block Name	BiasDigital => FPPA2000/Fpu_logic		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



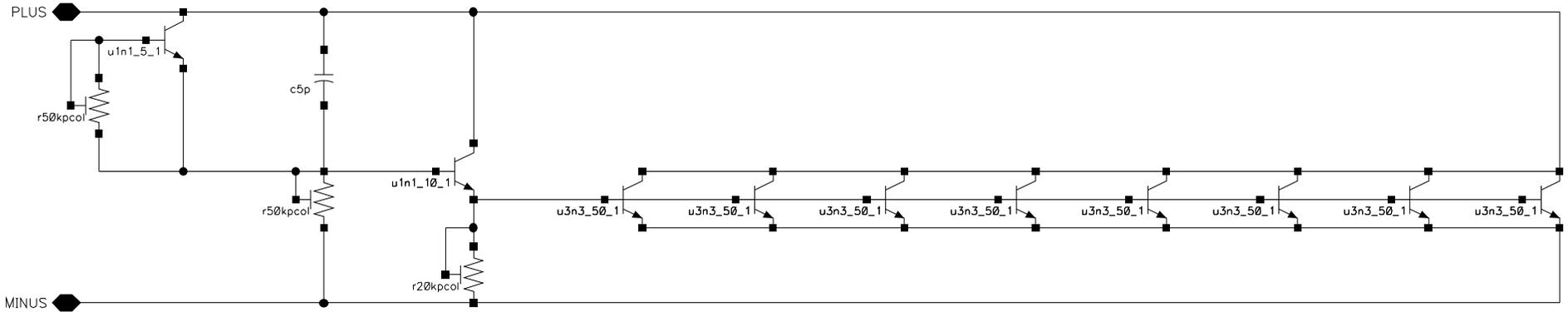
CMS-ECAL

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Project	Floating Point PreAmplier		
Library Name	FPPA2000		
Block Name	ProPad1	-> FPPA2000	
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	esd-protect -> FPPA2000		
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE



CMS-ECAL		Princeton University / IPNL	
Project	Floating Point PreAmplifier		
Library Name	FPPA2000		
Block Name	esd-clamp		-> FPPA2000
Last Changed			
Preliminary Design Review	TRUE	Critical Design Review	TRUE